



Alexandria University

Faculty of Engineering

Division of Communications & Electronics

EE 4E4 VLSI Modeling and Design Lab#1

Objectives

Upon the completion of this Lab, you should be able to:

1. Use L-edit software to lay out basic CMOS digital circuits,
2. Use T-spice software to analyze static and dynamic characteristics of CMOS digital circuits.

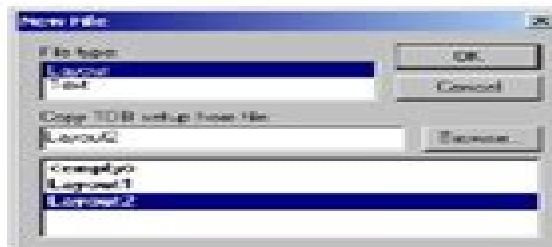
Requirements

You are required to draw the vertical and horizontal layouts for the basic CMOS inverter and compare between the two layouts in terms of the used area, power consumption, DC characteristics, and propagation delays. Both inverters should have the same dimensions.

The following sections provide the detailed procedures to draw the layout of the vertical CMOS inverter using L-edit.

Manual Layout

1. Copy the following files into your directory
Technology setup files for MOSIS/Orbit n-well 2.0 micron process.
(Technology
= SCNA, LAMBDA = 1.0 micron)
C:\canner\ed683\samples\tech\mosis\morbn20.
2. Launch L-Edit
3. Create New File. Create new files by choosing **File > New**, which opens the **New File** dialog:



Vlsi Lab Manual Using Tanner Eda

J Ma



Vlsi Lab Manual Using Tanner Eda:

ASIC & EDA, 1994 **IEEE Circuits & Devices**, 1994 *Laboratory Manual for Electronic Devices and Circuits* David A. Bell, 2001 This lab manual accompanies Electronic Devices and Circuits 4 e **Lab Manual for Hayes/Rosenberg's Data, Voice and Video Cabling, 3rd** Jim Hayes, 2008-07 The Lab Manual for DATA VOICE AND VIDEO CABLING 3rd Edition is a valuable tool designed to enhance your classroom experience Lab activities objectives materials lists step by step procedures illustrations review questions and more are all included *Digital Technology Lab Manual* Webb R J Staff, 1988-12 **Lab Manual** Gregory L. Moss, 1991 EVSL 195 Laboratory Manual LONG; AMY, SHEW; ROGER, EULIE; DEVON, 2016-08-18 EVSL 195 Laboratory Manual Global to Local Issues *Laboratory Manual for Use with Electricity and Electronics* Dale R. Patrick, William Dugger, 1996

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