V.34 Transmitter and Receiver Implementation on the TMS320C50 DSP

Application Report

Tms320c50 Dsp Manual

José D. P. Rolim

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Processing from a practical point of view with a working minimum of mathematics The emphasis is placed on the practical applications of DSP implementation issues tricks and pitfalls Intuitive explanations and appropriate examples are used to develop a fundamental understanding of DSP theory laying a firm foundation for the reader to pursue the matter further The reader will develop a clear understanding of DSP technology in a variety of fields from process control to communications Covers the use of DSP in different engineering sectors from communications to process control Ideal for a wide audience wanting to take advantage of the strong movement towards digital signal processing techniques in the engineering world Includes numerous practical exercises and diagrams covering many of the fundamental aspects of digital signal processing

Low-Power Processors and Systems on Chips Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet's recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

Controls, Automation of Communication Systems (ICCACS2004) ,2004

<u>Tiet.com-2000.</u> Surekha Bhanot,2000 **Digital Signal Processing** N. B. Jones, J. D. McK. Watson,1990 This volume presents the fundamentals of data signal processing ranging from data conversion to z transforms and spectral analysis In addition to presenting basic theory and describing the devices the material is complemented by real examples in specific case studies *Digital Signal Processing with Field Programmable Gate Arrays* Uwe Meyer-Baese,2007-09-28 A practical and fascinating book on a topic at the forefront of communications technology Field Programmable Gate Arrays FPGAs are on the verge of revolutionizing digital signal processing Novel FPGA families are replacing ASICs and PDSPs for front end digital signal processing algorithms at an accelerating rate The efficient implementation of these algorithms is the main goal of this book It starts with an overview of today s FPGA technology devices and tools for designing state of the art DSP systems Each of the book s chapter contains exercises The VERILOG source code and a glossary are given in the appendices

Passive, Active, and Digital Filters Wai-Kai Chen, 2005-08-24 Culled from the pages of CRC s highly successful best selling The Circuits and Filters Handbook Second Edition Passive Active and Digital Filters presents a sharply focused comprehensive review of the fundamental theory behind professional applications of these complex filters It supplies a concise convenient reference to the key concepts models and equations necessary to analyze design and predict the behavior of large scale systems that employ various types of filters illustrated by frequent examples Edited by a distinguished authority this book emphasizes the theoretical concepts underlying the processes behavior and operation of these filters More than 470 figures and tables illustrate the concepts and where necessary the theories principles and mathematics of some subjects are reviewed Expert contributors discuss general characteristics of filters frequency transformations sensitivity and selectivity low gain active filters higher order filters continuous time integrated filters FIR and IIR filters and VLSI implementation of digital filters among many other topics Passive Active and Digital Filters builds a strong theoretical foundation for the design and analysis of a variety of filters from passive to active to digital while serving as a handy reference for experienced engineers making it a must have for both beginners and seasoned experts Programmable Digital Signal Processors Yu Hen Hu, 2001-12-06 Presents the latest developments in the prgramming and design of programmable digital signal processors PDSPs with very long instruction word VLIW architecture algorithm formulation and implementation and modern applications for multimedia processing communications and industrial control

Hardware/Software Co-Design Giovanni DeMicheli, M.G. Sami, 2013-11-11 Concurrent design or co design of hardware and software is extremely important for meeting design goals such as high performance that are the key to commercial competitiveness Hardware Software Co Design covers many aspects of the subject including methods and examples for designing 1 general purpose and embedded computing systems based on instruction set processors 2 telecommunication systems using general purpose digital signal processors as well as application specific instruction set processors 3 embedded control systems and applications to automotive electronics. The book also surveys the areas of emulation and prototyping systems with field programmable gate array technologies hardware software synthesis and verification and industrial design trends Most contributions emphasize the design methodology the requirements and state of the art of computer aided co design tools together with current design examples The Application of Programmable DSPs in Mobile **Communications** Alan Gatherer, Edgar Auslander, 2001-12-21 With the introduction of WAP in Europe and I mode in Japan mobile terminals took their first steps out of the world of mobile telephony and into the world of mobile data At the same time the shift from 2nd generation to 3rd generation cellular technology has increased the potential data rate available to mobile users by tenfold as well as shifting data transport from circuit switched to packet data These fundamental shifts in nature and the quantity of data available to mobile users has led to an explosion in the number of applications being developed for future digital terminal devices Though these applications are diverse they share a common need for complex

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