

Turbo C User's Guide:

Embedded Assembly Language. Part I - Basic Principles

BASM DOC is on online file that tells you how to use the Turbo C++ built-in inline assembler (BASM) to include assembly language routines in your C and C++ programs without any need for a separate assembler. Such assembly language routines are called inline assembly, because they are compiled right along with your C routines, rather than being assembled separately, then linked together with modules produced by the C compiler.

Turbo C++ lets you write assembly language code right inside your C and C++ programs. This is known as inline assembly and has certain restrictions.

- It cannot use assembler macros
- It cannot handle 80386 or 80486 instructions
- · It does not permit Ideal mode syntax
- It allows only a limited set of assembler directives

To invoke the in-line assembler, you need only use the keyword asm to introduce an inline assembly language instruction. The format is

asm opcode operands

where opcode is a valid \$0x86 instruction, and operands contains the operand(s) acceptable to the opcode, and can reference C constants, variables, and labels

To include a number of asm statements, surround them with braces. The initial brace must appear on the same line as the asm keyword.

Semicolons are not used to start comments (as they are in TASM). When commenting asm statements, use C-style comments, like this

```
asm mov ax,ds, /* This comment is OK */
asm (pop ax, pop ds, iret,) /* This is legal too */
asm push ds :THIS COMMENT IS INVALID!!
```

The assembly language portion of the statement is copied straight to the output, embedded in the assembly language that Turbo C++ is generating from your C or C++ instructions. Any C symbols are replaced with ap-propriate assembly language equivalents.

Because the inline assembly facility is not a complete assembler, it may not accept some assembly language constructs. If this happens, Turbo C++ will issue an error message. You then have two choices. You can simplify your inline assembly language code so that the assembler will accept it, or you can use an external assembler such as TASM. However, TASM might not identify the location of errors, since the original C source line number is lost.

Turbo C User Guide

Richard Bailey

Turbo C User Guide:

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Mastering Turbo C Stan Kelly-Bootle, 1989 Power User's Guide to R:BASE Alan Simpson, Cheryl Currid, Craig A. Programming with Turbo C S. Scott Zimmerman, Beverly B. Zimmerman, 1989 Gillett, 1989 InfoWorld ,1989-05-22 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects Communication System Design Using DSP Algorithms Steven A. Tretter,1995-08-31 Primary focus is on communications systems Microprogrammed State Machine Design Michel A. Lynch, 2024-11-01 Microprogrammed State Machine Design is a digital computer architecture text that builds systematically from basic concepts to complex state machine design It provides practical techniques and alternatives for designing solutions to data processing problems both in commerce and in research purposes It offers an excellent introduction to the tools and elements of design used in microprogrammed state machines and incoporates the necessary background in number systems hardware building blocks assemblers for use in preparing control programs and tools and components for assemblers The author conducts an in depth examination of first and second level microprogrammed state machines He promotes a top down approach that examines algorithms mathematically to exploit the simplifications resulting from choosing the proper representation and application of algebraic manipulation. The steps involved in the cycle of design and simulation steps are demonstrated through an example of running a computer through a simulation Other topics covered in Microprogrammed State Machine Design include a discussion of simulation methods the development and use of assembler language processors and comparisons among various hardware implementations such as the Reduced Instruction Set Computer RISC and the Digital Signal Processor DSP As a text and guide Microprogrammed State Machine Design will interest students in the computer sciences computer architectects and engineers systems programmers and analysts and electrical engineers

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