

EXPERIEMENT NO. 2

Simulation using all the modeling styles and Synthesis of 1-bit half adder and 1-bit Full adder using verilog HDL

AIM:

Perform Zero Delay Simulation of 1-bit half adder and 1-bit Full adder written in behavioral, dataflow and structural modeling style in VERILOG HDL using a Test bench. Then, Synthesize each one of them on two different EDA tools.

Electronics Design Automation Tools used:

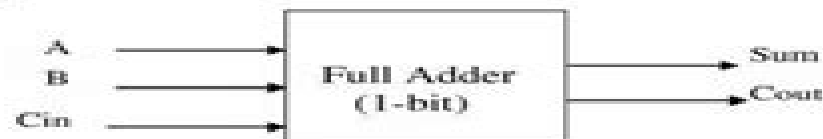
- i) FPGA Advantage 3.1 (includes Model Sim simulation tool and Leonardo Spectrum Synthesis Tool)
- ii) Xilinx Project Navigator 8.1 (Includes all the steps in the design flow from Simulation to Implementation to download onto FPGA).

Block Diagram:

1-bit Half Adder:



1-bit Full Adder:



Truth table:

Half Adder:

A	B	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

Full Adder:

A	B	Cin	Sum	Cout
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Programming Digital Applications W/Able, Vhdl-Lab Manual Han-Way Huang,2006-07 Digital Electronics Lab Manual with Vhdl Scott Stewart,2004-11-30 Digital Fundamentals with Cplds, Fpgas and Vhdl Laboratory Manual David Buchla,Douglas Joksch,2002-09-30 **Intelligent Systems** Bogdan M. Wilamowski,J. David Irwin,2018-10-03 The Industrial Electronics Handbook Second Edition combines traditional and newer more specialized knowledge that will help industrial electronics engineers develop practical solutions for the design and implementation of high power applications Embracing the broad technological scope of the field this collection explores fundamental areas including analog and digital circuits electronics electromagnetic machines signal processing and industrial control and communications systems It also facilitates the use of intelligent systems such as neural networks fuzzy systems and evolutionary methods in terms of a hierarchical structure that makes factory control and supervision more efficient by addressing the needs of all production components Enhancing its value this fully updated collection presents research and global trends as published in the IEEE Transactions on Industrial Electronics Journal one of the largest and most respected publications in the field As intelligent systems continue to replace and sometimes outperform human intelligence in decision making processes they have made substantial contributions to the solution of very complex problems As a result the field of computational intelligence has branched out in several directions For instance artificial neural networks can learn how to classify patterns such as images or sequences of events and effectively model complex nonlinear systems Simple and easy to implement fuzzy systems can be applied to successful modeling and system control Illustrating how these and other tools help engineers model nonlinear system behavior determine and evaluate system parameters and ensure overall system control Intelligent Systems Addresses various aspects of neural networks and fuzzy systems Focuses on system optimization covering new techniques such as evolutionary methods swarm and ant colony optimizations Discusses several applications that deal with methods of computational intelligence Other volumes in the set Fundamentals of Industrial Electronics Power Electronics and Motor Drives Control and Mechatronics Industrial Communication Systems **The Industrial Electronics Handbook - Five Volume Set** Bogdan M. Wilamowski,J. David Irwin,2011-03-04 Industrial electronics systems govern so many different functions that vary in complexity from the operation of relatively simple applications such as electric motors to that of more complicated machines and systems including robots and entire fabrication processes The Industrial Electronics Handbook Second Edition combines traditional and new **Acceleration of Biomedical Image Processing with Dataflow on FPGAs** Frederik Grüll,Udo Kobschull,2022-09-01 Short compute times are crucial for timely diagnostics in biomedical applications but lead to a high demand in computing for new and improved imaging techniques In this book reconfigurable computing with FPGAs is discussed as an alternative to multi core processing and graphics card accelerators Instead of adjusting the application to the hardware FPGAs allow the hardware to also be adjusted to the problem Acceleration of Biomedical Image Processing with

Dataflow on FPGAs covers the transformation of image processing algorithms towards a system of deep pipelines that can be executed with very high parallelism The transformation process is discussed from initial design decisions to working implementations Two example applications from stochastic localization microscopy and electron tomography illustrate the approach further Topics discussed in the book include Reconfigurable hardware Dataflow computing Image processing Application acceleration

Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm Reiner W. Hartenstein, Andres Keevallik, 2003-06-29 This book constitutes the refereed proceedings of the 8th International Workshop on Field Programmable Logics and Applications FPL 98 held in Tallinn Estonia in August September 1998 The 39 revised full papers presented were carefully selected for inclusion in the book from a total of 86 submissions Also included are 30 refereed high quality posters The papers are organized in topical sections on design methods general aspects prototyping and simulation development methods accelerators system architectures hardware software codesign system development algorithms on FPGAs and applications

Transactions of the Society for Modeling and Simulation International ,2000

Microelectronics Education B. Courtois, N. Guillemot, G. Kamarinos, G. Stéhelin, 2013-03-09 This is the third edition of the European Workshop on Microelectronics Education EWME A steady state regime has now been reached An international community of university teachers is constituted they exchange their experience and their pedagogical tools They discuss the best ways to transfer the rapidly changing techniques to their students and to introduce them to the new physical and mathematical concepts and models for the innovative techniques devices circuits and design methods The number of abstracts submitted to EWME 2000 about one hundred enabled the scientific committee to proceed to a clear selection EWME is a European meeting Indeed authors from 20 different European countries contribute to this volume Nevertheless the participation of authors from Brazil Canada China New Zealand and USA shows that the workshop gradually attains an international dimension th The 20 century can be characterized as the century of electron The electron as an elementary particle was discovered by J J Thomson in 1897 and was rapidly used to transfer energy and information Thanks to electron universe and micro cosmos could be explored Electron became the omnipotent and omnipresent almost immaterial angel of our W orld This was made possible thanks to electronics and for the last 30 years to microelectronics Microelectronics not only modified and even radically transformed the industrial and the every day landscapes but it also led to the so called information revolution with which begins the 21 st century

Modelling and Simulation 1992 John Stephenson, 1992

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology ,1999

Field-programmable Logic and Applications ,1998

Forthcoming Books Rose Arny, 1997-12

Digital Logic Simulation and CPLD Programming with VHDL Steve Waterman, 2003 For freshman level courses in Introduction to Digital Electronics sophomore level courses in Introduction to Microprocessors and other middle upper level courses in Digital Electronics This lab manual written around software and hardware developments of the past ten years focuses on the fundamentals of digital electronics and use

of Max Plus II software by Altera Corporation Lab sequences start with digital gates and logic control circuits progress to MSI devices latches and flip flops and cover clock dependent circuits and LPM_MEGA functions available in the software

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Covers all aspects of the VHDL language **Who's Who in Science and Engineering 2008-2009** Who's Who
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,1991 **Instructor's Resource Manual to Accompany Digital Fundamentals with VHDL** Thomas L. Floyd,2003

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