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Very Large Scale Integration Algorithms And Architectures International Workshop Proceedings

Santosh K. Kurinec, Sumeet Walia



Very Large Scale Integration Algorithms And Architectures International Workshop Proceedings:

Algorithms and Architectures for Cryptography and Source Coding in Non-Volatile Flash Memories Malek Safieh, 2021-08-09 In this work algorithms and architectures for cryptography and source coding are developed which are suitable for many resource constrained embedded systems such as non volatile flash memories A new concept for elliptic curve cryptography is presented which uses an arithmetic over Gaussian integers Gaussian integers are a subset of the complex numbers with integers as real and imaginary parts Ordinary modular arithmetic over Gaussian integers is computational expensive To reduce the complexity a new arithmetic based on the Montgomery reduction is presented For the elliptic curve point multiplication this arithmetic over Gaussian integers improves the computational efficiency the resistance against side channel attacks and reduces the memory requirements Furthermore an efficient variant of the Lempel Ziv Welch LZW algorithm for universal lossless data compression is investigated Instead of one LZW dictionary this algorithm applies several dictionaries to speed up the encoding process Two dictionary partitioning techniques are introduced that improve the compression rate and reduce the memory size of this parallel dictionary LZW algorithm Computer And Network Technology - Proceedings Of The International Conference On Iccnt 2009 Venkatesh Mahadevan, Jianhong Zhou, 2009-07-16 ICCNT is the main annual computer and network research conference in Chennai that presents cutting edge research work It will act as a platform for scientists scholars engineers and students from universities all around the world to present ongoing research and hence foster better research relations between universities and the computer and networking industry

Embedded and Networking Systems Gul N. Khan, Krzysztof Iniewski, 2017-07-12 *Embedded and Networking Systems Design Software and Implementation* explores issues related to the design and synthesis of high performance embedded computer systems and networks The emphasis is on the fundamental concepts and analytical techniques that are applicable to a range of embedded and networking applications rather than on specific embedded architectures software development or system level integration This system point of view guides designers in dealing with the trade offs to optimize performance power cost and other system level non functional requirements The book brings together contributions by researchers and experts from around the world offering a global view of the latest research and development in embedded and networking systems Chapters highlight the evolution and trends in the field and supply a fundamental and analytical understanding of some underlying technologies Topics include the co design of embedded systems code optimization for a variety of applications power and performance trade offs benchmarks for evaluating embedded systems and their components and mobile sensor network systems The book also looks at novel applications such as mobile sensor systems and video networks A comprehensive review of groundbreaking technology and applications this book is a timely resource for system designers researchers and students interested in the possibilities of embedded and networking systems It gives readers a better understanding of an emerging technology evolution that is helping drive telecommunications into the next decade *The*

Circuits and Filters Handbook (Five Volume Slipcase Set) Wai-Kai Chen, 2018-12-14 Standard setting groundbreaking authoritative comprehensive these often overused words perfectly describe The Circuits and Filters Handbook Third Edition This standard setting resource has documented the momentous changes that have occurred in the field of electrical engineering providing the most comprehensive coverage available More than 150 contributing experts offer in depth insights and enlightened perspectives into standard practices and effective techniques that will make this set the first and most likely the only tool you select to help you with problem solving In its third edition this groundbreaking bestseller surveys accomplishments in the field providing researchers and designers with the comprehensive detail they need to optimize research and design All five volumes include valuable information on the emerging fields of circuits and filters both analog and digital Coverage includes key mathematical formulas concepts definitions and derivatives that must be mastered to perform cutting edge research and design The handbook avoids extensively detailed theory and instead concentrates on professional applications with numerous examples provided throughout The set includes more than 2500 illustrations and hundreds of references Available as a comprehensive five volume set each of the subject specific volumes can also be purchased separately

High Performance Computing for Big Data Chao Wang, 2017-10-16 High Performance Computing for Big Data Methodologies and Applications explores emerging high performance architectures for data intensive applications novel efficient analytical strategies to boost data processing and cutting edge applications in diverse fields such as machine learning life science neural networks and neuromorphic engineering The book is organized into two main sections The first section covers Big Data architectures including cloud computing systems and heterogeneous accelerators It also covers emerging 3D IC design principles for memory architectures and devices The second section of the book illustrates emerging and practical applications of Big Data across several domains including bioinformatics deep learning and neuromorphic engineering Features Covers a wide range of Big Data architectures including distributed systems like Hadoop Spark Includes accelerator based approaches for big data applications such as GPU based acceleration techniques and hardware acceleration such as FPGA CGRA ASICs Presents emerging memory architectures and devices such as NVM STT RAM 3D IC design principles Describes advanced algorithms for different big data application domains Illustrates novel analytics techniques for Big Data applications scheduling mapping and partitioning methodologies Featuring contributions from leading experts this book presents state of the art research on the methodologies and applications of high performance computing for big data applications About the Editor Dr Chao Wang is an Associate Professor in the School of Computer Science at the University of Science and Technology of China He is the Associate Editor of ACM Transactions on Design Automations for Electronics Systems TODAES Applied Soft Computing Microprocessors and Microsystems IET Computers Digital Techniques and International Journal of Electronics Dr Chao Wang was the recipient of Youth Innovation Promotion Association CAS ACM China Rising Star Honorable Mention 2016 and best IP nomination of DATE 2015 He is now

on the CCF Technical Committee on Computer Architecture CCF Task Force on Formal Methods He is a Senior Member of IEEE Senior Member of CCF and a Senior Member of ACM Autonomic Networking-on-Chip Phan Cong-Vinh, 2018-09-03 Despite the growing mainstream importance and unique advantages of autonomic networking on chip ANoC technology Autonomic Networking On Chip Bio Inspired Specification Development and Verification is among the first books to evaluate research results on formalizing this emerging NoC paradigm which was inspired by the human nervous system The FIRST Book to Assess Research Results Opportunities Trends in BioChipNets The third book in the Embedded Multi Core Systems series from CRC Press this is an advanced technical guide and reference composed of contributions from prominent researchers in industry and academia around the world A response to the critical need for a global information exchange and dialogue it is written for engineers scientists practitioners and other researchers who have a basic understanding of NoC and are now ready to learn how to specify develop and verify ANoC using rigorous approaches Offers Expert Insights Into Technical Topics Including Bio inspired NoC How to map applications onto ANoC ANoC for FPGAs and structured ASICs Methods to apply formal methods in ANoC development Ways to formalize languages that enable ANoC Methods to validate and verify techniques for ANoC Use of self processes in ANoC self organization configuration healing optimization protection etc Use of calculi for reasoning about context awareness and programming models in ANoC With illustrative figures to simplify contents and enhance understanding this resource contains original peer reviewed chapters reporting on new developments and opportunities emerging trends and open research problems of interest to both the autonomic computing and network on chip communities Coverage includes state of the art ANoC architectures protocols technologies and applications This volume thoroughly explores the theory behind ANoC to illustrate strategies that enable readers to use formal ANoC methods yet still make sound judgments and allow for reasonable justifications in practice Wireless Security and Cryptography Nicolas Sklavos, Xinmiao Zhang, 2017-12-19 As the use of wireless devices becomes widespread so does the need for strong and secure transport protocols Even with this intensified need for securing systems using cryptography does not seem to be a viable solution due to difficulties in implementation The security layers of many wireless protocols use outdated encryption algorithms which have proven unsuitable for hardware usage particularly with handheld devices Summarizing key issues involved in achieving desirable performance in security implementations Wireless Security and Cryptography Specifications and Implementations focuses on alternative integration approaches for wireless communication security It gives an overview of the current security layer of wireless protocols and presents the performance characteristics of implementations in both software and hardware This resource also presents efficient and novel methods to execute security schemes in wireless protocols with high performance It provides the state of the art research trends in implementations of wireless protocol security for current and future wireless communications Unique in its coverage of specification and implementation concerns that include hardware design techniques Wireless Security and Cryptography

Specifications and Implementations provides thorough coverage of wireless network security and recent research directions in the field

Hardware Acceleration of EDA Algorithms Sunil P Khatri, Kanupriya Gulati, 2010-03-11 Single threaded software applications have ceased to see significant gains in performance on a general purpose CPU even with further scaling in very large scale integration VLSI technology This is a significant problem for electronic design automation EDA applications since the design complexity of VLSI integrated circuits ICs is continuously growing In this research monograph we evaluate custom ICs old programmable gate arrays FPGAs and graphics processors as platforms for accelerating EDA algorithms instead of the general purpose single threaded CPU We study applications which are used in key time consuming steps of the VLSI design Now Further these applications also have different degrees of inherent parallelism in them We study both control dominated EDA applications and control plus data parallel EDA applications We accelerate these applications on these different hardware platforms We also present an automated approach for accelerating certain uniprocessor applications on a graphics processor This monograph compares custom ICs FPGAs and graphics processing units GPUs as potential platforms to accelerate EDA algorithms It also provides details of the programming model used for interfacing with the GPUs

Low-Energy FPGAs — Architecture and Design Varghese George, Jan M. Rabaey, 2012-12-06 Low Energy FPGAs Architecture and Design is a primary resource for both researchers and practicing engineers in the field of digital circuit design The book addresses the energy consumption of Field Programmable Gate Arrays FPGAs FPGAs are becoming popular as embedded components in computing platforms The programmability of the FPGA can be used to customize implementations of functions on an application basis This leads to performance gains and enables reuse of expensive silicon Chapter 1 provides an overview of digital circuit design and FPGAs Chapter 2 looks at the implication of deep submicron technology on FPGA power dissipation Chapter 3 describes the exploration environment to guide and evaluate design decisions Chapter 4 discusses the architectural optimization process to evaluate the trade offs between the flexibility of the architecture and the effect on the performance metrics Chapter 5 reviews different circuit techniques to reduce the performance overhead of some of the dominant components Chapter 6 shows methods to configure FPGAs to minimize the programming overhead Chapter 7 addresses the physical realization of some of the critical components and the final implementation of a specific low energy FPGA Chapter 8 compares the prototype array to an equivalent commercial architecture

Energy Efficient Computing & Electronics Santosh K. Kurinec, Sumeet Walia, 2019-01-31 In our abundant computing infrastructure performance improvements across most all application spaces are now severely limited by the energy dissipation involved in processing storing and moving data The exponential increase in the volume of data to be handled by our computational infrastructure is driven in large part by unstructured data from countless sources This book explores revolutionary device concepts associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation from device to circuit to system level With chapters written by

international experts in their corresponding field the text investigates new approaches to lower energy requirements in computing Features Has a comprehensive coverage of various technologies Written by international experts in their corresponding field Covers revolutionary concepts at the device circuit and system levels **Innovations in Embedded and Real-Time Systems Engineering for Communication** Virtanen, Seppo, 2012-04-30 This book has collected the latest research within the field of real time systems engineering and will serve as a vital reference compendium for practitioners and academics Provided by publisher *Algorithms, Complexity Analysis and VLSI Architectures for MPEG-4 Motion Estimation* Peter M. Kuhn, 2013-06-29 MPEG 4 is the multimedia standard for combining interactivity natural and synthetic digital video audio and computer graphics Typical applications are internet video conferencing mobile videophones multimedia cooperative work teleteaching and games With MPEG 4 the next step from block based video ISO IEC MPEG 1 MPEG 2 CCITT H 261 ITU T H 263 to arbitrarily shaped visual objects is taken This significant step demands a new methodology for system analysis and design to meet the considerably higher flexibility of MPEG 4 Motion estimation is a central part of MPEG 1 2 4 and H 261 H 263 video compression standards and has attracted much attention in research and industry for the following reasons it is computationally the most demanding algorithm of a video encoder about 60 80% of the total computation time it has a high impact on the visual quality of a video encoder and it is not standardized thus being open to competition *Algorithms Complexity Analysis and VLSI Architectures for MPEG 4 Motion Estimation* covers in detail every single step in the design of a MPEG 1 2 4 or H 261 H 263 compliant video encoder Fast motion estimation algorithms Complexity analysis tools Detailed complexity analysis of a software implementation of MPEG 4 video Complexity and visual quality analysis of fast motion estimation algorithms within MPEG 4 Design space on motion estimation VLSI architectures Detailed VLSI design examples of 1 a high throughput and 2 a low power MPEG 4 motion estimator *Algorithms Complexity Analysis and VLSI Architectures for MPEG 4 Motion Estimation* is an important introduction to numerous algorithmic architectural and system design aspects of the multimedia standard MPEG 4 As such all researchers students and practitioners working in image processing video coding or system and VLSI design will find this book of interest

Three-Dimensional Integrated Circuit Design Vasilis F. Pavlidis, Ioannis Savidis, Eby G. Friedman, 2017-07-04 Three Dimensional Integrated Circuit Design Second Edition expands the original with more than twice as much new content adding the latest developments in circuit models temperature considerations power management memory issues and heterogeneous integration 3 D IC experts Pavlidis Savidis and Friedman cover the full product development cycle throughout the book emphasizing not only physical design but also algorithms and system level considerations to increase speed while conserving energy A handy comprehensive reference or a practical design guide this book provides effective solutions to specific challenging problems concerning the design of three dimensional integrated circuits Expanded with new chapters and updates throughout based on the latest research in 3 D integration Manufacturing techniques for 3 D ICs with TSVs

Electrical modeling and closed form expressions of through silicon vias Substrate noise coupling in heterogeneous 3 D ICs Design of 3 D ICs with inductive links Synchronization in 3 D ICs Variation effects on 3 D ICs Correlation of WID variations for intra tier buffers and wires Offers practical guidance on designing 3 D heterogeneous systems Provides power delivery of 3 D ICs Demonstrates the use of 3 D ICs within heterogeneous systems that include a variety of materials devices processors GPU CPU integration and more Provides experimental case studies in power delivery synchronization and thermal characterization

Microfluidic Very Large Scale Integration (VLSI) Paul Pop,Wajid Hassan Minhass,Jan Madsen,2016-02-08 This book presents the state of the art techniques for the modeling simulation testing compilation and physical synthesis of mVLSI biochips The authors describe a top down modeling and synthesis methodology for the mVLSI biochips inspired by microelectronics VLSI methodologies They introduce a modeling framework for the components and the biochip architecture and a high level microfluidic protocol language Coverage includes a topology graph based model for the biochip architecture and a sequencing graph to model for biochemical application showing how the application model can be obtained from the protocol language The techniques described facilitate programmability and automation enabling developers in the emerging large biochip market

Algorithms and Parallel VLSI Architectures Ed F. Deprettere,Alle-Jan van der Veen,1991

Reconfigurable Computing: Architectures and Applications Koen Bertels,2006-07-26 This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Reconfigurable Computing ARC 2006 held in Delft The Netherlands in March 2006 The 22 revised full papers and 35 revised short papers presented were thoroughly reviewed and selected from 95 submissions The papers are organized in topical sections on applications power image processing organization and architecture networks and communication security and tools

VLSI-SoC 2023: Innovations for Trustworthy Artificial Intelligence Ibrahim (Abe) M. Elfadel,Lutfi Albasha,2024-12-28 This book contains revised and extended versions of a selection of papers presented at the 31st IFIP WG 10 5 IEEE International Conference on the theme of VLSI SoC Innovations for Trustworthy Artificial Intelligence VLSI SoC 2023 held in Sharjah United Arab Emirates during October 16 18 2023 The 15 full papers included in this volume were carefully revised and expanded from 77 papers submitted to the conference This edited volume has been organized into four parts architectures accelerators resiliency and robustness and security and privacy and is representing cutting edge research at the forefront of VLSI technology

Retargetable Processor System Integration into Multi-Processor System-on-Chip Platforms Andreas Wieferink,Heinrich Meyr,Rainer Leupers,2008-07-08 This book presents a methodology and the associated tooling for enabling design space exploration as well as a successive refinement flow for the design of optimized MP SoCs with a high degree of automation

VLSI-SoC: Research Trends in VLSI and Systems on Chip Giovanni De Micheli,Salvador Mir,Ricardo Reis,2010-08-23 This book contains extended and revised versions of the best papers presented during the fourteenth IFIP TC 10 WG 10 5 International Conference on Very Large Scale Integration This conference provides a forum

to exchange ideas and show industrial and academic research results in microelectronics design The current trend toward increasing chip integration and technology process advancements brings about stimulating new challenges both at the physical and system design levels **Scientific and Technical Aerospace Reports** ,1989

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