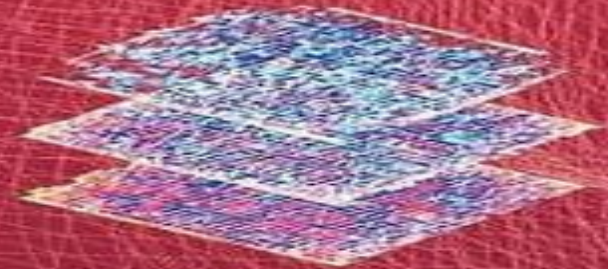


Yuan Xie
Jason Cong
Sachin Sapatnekar
Editors

Three-Dimensional Integrated Circuit Design

EDA, Design and
Microarchitectures



Three Dimensional Integrated Circuit Design EDA Design And Microarchitectures Integrated Circuits And Systems

**Yuan Xie,Jingsheng Jason Cong,Sachin
Sapatnekar**



Three Dimensional Integrated Circuit Design Eda Design And Microarchitectures Integrated Circuits And Systems:

Three-Dimensional Integrated Circuit Design Yuan Xie,Jingsheng Jason Cong,Sachin Sapatnekar,2009-12-02 We live in a time of great change In the electronics world the last several decades have seen unprecedented growth and advancement described by Moore s law This observation stated that transistor density in integrated circuits doubles every 1 5 2 years This came with the simultaneous improvement of individual device perf mance as well as the reduction of device power such that the total power of the resulting ICs remained under control No trend remains constant forever and this is unfortunately the case with Moore s law The trouble began a number of years ago when CMOS devices were no longer able to proceed along the classical scaling trends Key device parameters such as gate oxide thickness were simply no longer able to scale As a result device o state currents began to creep up at an alarming rate These continuing problems with classical scaling have led to a leveling off of IC clock speeds to the range of several GHz Of course chips can be clocked higher but the thermal issues become unmanageable This has led to the recent trend toward microprocessors with mul ple cores each running at a few GHz at the most The goal is to continue improving performance via parallelism by adding more and more cores instead of increasing speed The challenge here is to ensure that general purpose codes can be ef ciently parallelized There is another potential solution to the problem of how to improve CMOS technology performance three dimensional integrated circuits 3D ICs

Three-Dimensional Integrated Circuit Design Yuan Xie,Jingsheng Jason Cong,Sachin Sapatnekar,2010-05-19 We live in a time of great change In the electronics world the last several decades have seen unprecedented growth and advancement described by Moore s law This observation stated that transistor density in integrated circuits doubles every 1 5 2 years This came with the simultaneous improvement of individual device perf mance as well as the reduction of device power such that the total power of the resulting ICs remained under control No trend remains constant forever and this is unfortunately the case with Moore s law The trouble began a number of years ago when CMOS devices were no longer able to proceed along the classical scaling trends Key device parameters such as gate oxide thickness were simply no longer able to scale As a result device o state currents began to creep up at an alarming rate These continuing problems with classical scaling have led to a leveling off of IC clock speeds to the range of several GHz Of course chips can be clocked higher but the thermal issues become unmanageable This has led to the recent trend toward microprocessors with mul ple cores each running at a few GHz at the most The goal is to continue improving performance via parallelism by adding more and more cores instead of increasing speed The challenge here is to ensure that general purpose codes can be ef ciently parallelized There is another potential solution to the problem of how to improve CMOS technology performance three dimensional integrated circuits 3D ICs

Design and Modeling for 3D ICs and Interposers Madhavan Swaminathan,2013-11-05 3D Integration is being touted as the next semiconductor revolution This book provides a comprehensive coverage on the design

and modeling aspects of 3D integration in particular focus on its electrical behavior Looking from the perspective the Silicon Via TSV and Glass Via TGV technology the book introduces 3DICs and Interposers as a technology and presents its application in numerical modeling signal integrity power integrity and thermal integrity The authors underscored the potential of this technology in design exchange formats and power distribution

Three Dimensional System

Integration Antonis Papanikolaou, Dimitrios Soudris, Riko Radojicic, 2010-12-07 Three dimensional 3D integrated circuit IC stacking is the next big step in electronic system integration It enables packing more functionality as well as integration of heterogeneous materials devices and signals in the same space volume This results in consumer electronics e g mobile handheld devices which can run more powerful applications such as full length movies and 3D games with longer battery life This technology is so promising that it is expected to be a mainstream technology a few years from now less than 10 15 years from its original conception To achieve this type of end product changes in the entire manufacturing and design process of electronic systems are taking place This book provides readers with an accessible tutorial on a broad range of topics essential to the non expert in 3D System Integration It is an invaluable resource for anybody in need of an overview of the 3D manufacturing and design chain

Arbitrary Modeling of TSVs for 3D Integrated Circuits

Khaled Salah, Yehea Ismail, Alaa El-Rouby, 2014-08-21 This book presents a wide band and technology independent SPICE compatible RLC model for through silicon vias TSVs in 3D integrated circuits This model accounts for a variety of effects including skin effect depletion capacitance and nearby contact effects Readers will benefit from in depth coverage of concepts and technology such as 3D integration Macro modeling dimensional analysis and compact modeling as well as closed form equations for the through silicon via parasitics Concepts covered are demonstrated by using TSVs in applications such as a spiral inductor and inductive based communication system and bandpass filtering

Three-dimensional Integrated Circuit Design

Vasilis F. Pavlidis, Eby G. Friedman, 2010-07-28 With vastly increased complexity and functionality in the nanometer era i e hundreds of millions of transistors on one chip increasing the performance of integrated circuits has become a challenging task Connecting effectively interconnect design all of these chip elements has become the greatest determining factor in overall performance 3 D integrated circuit design may offer the best solutions in the near future This is the first book on 3 D integrated circuit design covering all of the technological and design aspects of this emerging design paradigm while proposing effective solutions to specific challenging problems concerning the design of 3 D integrated circuits A handy comprehensive reference or a practical design guide this book provides a sound foundation for the design of 3 D integrated circuits Demonstrates how to overcome interconnect bottleneck with 3 D integrated circuit design leading edge design techniques offer solutions to problems performance power consumption price faced by all circuit designers The FIRST book on 3 D integrated circuit design provides up to date information that is otherwise difficult to find Focuses on design issues key to the product development cycle good design plays a major role in exploiting the implementation flexibilities offered in

the 3 D Provides broad coverage of 3 D integrated circuit design including interconnect prediction models thermal management techniques and timing optimization offers practical view of designing 3 D circuits Designing TSVs for 3D Integrated Circuits Nauman Khan,Soha Hassoun,2012-09-23 This book explores the challenges and presents best strategies for designing Through Silicon Vias TSVs for 3D integrated circuits It describes a novel technique to mitigate TSV induced noise the GND Plug which is superior to others adapted from 2 D planar technologies such as a backside ground plane and traditional substrate contacts The book also investigates in the form of a comparative study the impact of TSV size and granularity spacing of C4 connectors off chip power delivery network shared and dedicated TSVs and coaxial TSVs on the quality of power delivery in 3 D ICs The authors provide detailed best design practices for designing 3 D power delivery networks Since TSVs occupy silicon real estate and impact device density this book provides four iterative algorithms to minimize the number of TSVs in a power delivery network Unlike other existing methods these algorithms can be applied in early design stages when only functional block level behaviors and a floorplan are available Finally the authors explore the use of Carbon Nanotubes for power grid design as a futuristic alternative to Copper Three-Dimensional Integrated Circuits Guangyu Sun,Yibo Chen,Xiangyu Dong,Yuan Xie,Jin Ouyang,2011-05 Presents the background on 3D integration technology and shows the major benefits offered by 3D integration EDA design tools and methodologies for 3D ICs are reviewed The cost of 3D integration is also analyzed 3D IC Stacking Technology Banqiu Wu,Ajay Kumar,Sesh Ramaswami,2011-07-07 The latest advances in three dimensional integrated circuit stacking technology With a focus on industrial applications 3D IC Stacking Technology offers comprehensive coverage of design test and fabrication processing methods for three dimensional device integration Each chapter in this authoritative guide is written by industry experts and details a separate fabrication step Future industry applications and cutting edge design potential are also discussed This is an essential resource for semiconductor engineers and portable device designers 3D IC Stacking Technology covers High density through silicon stacking TSS technology Practical design ecosystem for heterogeneous 3D IC products Design automation and TCAD tool solutions for through silicon via TSV based 3D IC stack Process integration for TSV manufacturing High aspect ratio silicon etch for TSV Dielectric deposition for TSV Barrier and seed deposition Copper electrodeposition for TSV Chemical mechanical polishing for TSV applications Temporary and permanent bonding Assembly and test aspects of TSV technology Die-stacking Architecture Yuan Xie,Jishen Zhao,2022-05-31 The emerging three dimensional 3D chip architectures with their intrinsic capability of reducing the wire length promise attractive solutions to reduce the delay of interconnects in future microprocessors 3D memory stacking enables much higher memory bandwidth for future chip multiprocessor design mitigating the memory wall problem In addition heterogenous integration enabled by 3D technology can also result in innovative designs for future microprocessors This book first provides a brief introduction to this emerging technology and then presents a variety of approaches to designing future 3D microprocessor systems by leveraging the

benefits of low latency high bandwidth and heterogeneous integration capability which are offered by 3D technology

Advancing Uncertain Combinatorics through Graphization, Hyperization, and Uncertainization: Fuzzy, Neutrosophic, Soft, Rough, and Beyond Takaaki Fujita, Florentin Smarandache, 2025-01-20 This book is the fifth volume in the series of Collected Papers on Advancing Uncertain Combinatorics through Graphization Hyperization and Uncertainization Fuzzy Neutrosophic Soft Rough and Beyond This volume specifically delves into the concept of Various SuperHyperConcepts building on the foundational advancements introduced in previous volumes The series aims to explore the ongoing evolution of uncertain combinatorics through innovative methodologies such as graphization hyperization and uncertainization These approaches integrate and extend core concepts from fuzzy neutrosophic soft and rough set theories providing robust frameworks to model and analyze the inherent complexity of real world uncertainties At the heart of this series lies combinatorics and set theory cornerstones of mathematics that address the study of counting arrangements and the relationships between collections under defined rules Traditionally combinatorics has excelled in solving problems involving uncertainty while advancements in set theory have expanded its scope to include powerful constructs like fuzzy and neutrosophic sets These advanced sets bring new dimensions to uncertainty modeling by capturing not just binary truth but also indeterminacy and falsity In this fifth volume the exploration of Various SuperHyperConcepts provides an innovative lens to address uncertainty complexity and hierarchical relationships It synthesizes key methodologies introduced in earlier volumes such as hyperization and neutrosophic extensions while advancing new theories and applications From pioneering hyperstructures to applications in advanced decision making language modeling and neural networks this book represents a significant leap forward in uncertain combinatorics and its practical implications across disciplines The book is structured into 17 chapters each contributing unique perspectives and advancements in the realm of Various SuperHyperConcepts and their related frameworks Chapter 1 introduces the concept of Body Mind Soul Spirit Fluidity within psychology and phenomenology while examining established social science frameworks like PDCA and DMAIC It extends these frameworks using Neutrosophic Sets a flexible extension of Fuzzy Sets to improve their adaptability for mathematical and programming applications The chapter emphasizes the potential of Neutrosophic theory to address multi dimensional challenges in social sciences Chapter 2 delves into the theoretical foundation of Hyperfunctions and their generalizations such as Hyperrandomness and Hyperdecision Making It explores higher order frameworks like Weak Hyperstructures Hypergraphs and Cognitive Hypermaps aiming to establish their versatility in addressing multi layered problems and setting a foundation for further studies Chapter 3 extends traditional decision making methodologies into HyperDecision Making and n SuperHyperDecision Making By building on approaches like MCDM and TOPSIS this chapter develops frameworks capable of addressing complex decision making scenarios emphasizing their applicability in dynamic multi objective contexts Chapter 4 explores integrating uncertainty frameworks including Fuzzy Neutrosophic and Plithogenic Sets into Large Language

Models LLMs It proposes innovative models like Large Uncertain Language Models and Natural Uncertain Language Processing integrating hierarchical and generalized structures to advance the handling of uncertainty in linguistic representation and processing Chapter 5 introduces the Natural n Superhyper Plithogenic Language by synthesizing natural language plithogenic frameworks and superhyperstructures This innovative construct seeks to address challenges in advanced linguistic and structural modeling blending attributes of uncertainty complexity and hierarchical abstraction Chapter 6 defines mathematical extensions such as NeutroHyperstructures and AntiHyperstructures using the Neutrosophic Triplet framework It formalizes structures like neutro superhyperstructures advancing classical frameworks into higher dimensional realms Chapter 7 explores the extension of Binary Code Gray Code and Floorplans through hyperstructures and superhyperstructures It highlights their iterative and hierarchical applications demonstrating their adaptability for complex data encoding and geometric arrangement challenges Chapter 8 investigates the Neutrosophic TwoFold SuperhyperAlgebra combining classical algebraic operations with neutrosophic components This chapter expands upon existing algebraic structures like Hyperalgebra and AntiAlgebra exploring hybrid frameworks for advanced mathematical modeling Chapter 9 introduces Hyper Z Numbers and SuperHyper Z Numbers by extending the traditional Z Number framework with hyperstructures These extensions aim to represent uncertain information in more complex and multidimensional contexts Chapter 10 revisits category theory through the lens of hypercategories and superhypercategories By incorporating hierarchical and iterative abstractions this chapter extends the foundational principles of category theory to more complex and layered structures Chapter 11 formalizes the concept of n SuperHyperBranch width and its theoretical properties By extending hypergraphs into superhypergraphs the chapter explores recursive structures and their potential for representing intricate hierarchical relationships Chapter 12 examines superhyperstructures of partitions integrals and spaces proposing a framework for advancing mathematical abstraction It highlights the potential applications of these generalizations in addressing hierarchical and multi layered problems Chapter 13 revisits Rough HyperRough and SuperHyperRough Sets introducing new concepts like Tree HyperRough Sets The chapter connects these frameworks to advanced approaches for modeling uncertainty and complex relationships Chapter 14 explores Plithogenic SuperHyperStructures and their applications in decision making control and neuro systems By integrating these advanced frameworks the chapter proposes innovative directions for extending existing systems to handle multi attribute and contradictory properties Chapter 15 focuses on superhypergraphs expanding hypergraph concepts to model complex structural types like arboreal and molecular superhypergraphs It introduces Generalized n th Powersets as a unifying framework for broader mathematical applications while also touching on hyperlanguage processing Chapter 16 defines NeutroHypergeometry and AntiHypergeometry as extensions of classical geometric structures Using the Geometric Neutrosophic Triplet the chapter demonstrates the flexibility of these frameworks in representing multi dimensional and uncertain relationships Chapter 17 establishes the

theoretical groundwork for SuperHyperGraph Neural Networks and Plithogenic Graph Neural Networks By integrating advanced graph structures this chapter opens pathways for applying neural networks to more intricate and uncertain data representations VLSI Design Esteban Tlelo-Cuautle, Sheldon X.-D. Tan, 2012-01-20 This book provides some recent advances in design nanometer VLSI chips The selected topics try to present some open problems and challenges with

important topics ranging from design tools new post silicon devices GPU based parallel computing emerging 3D integration and antenna design The book consists of two parts with chapters such as VLSI design for multi sensor smart systems on a chip Three dimensional integrated circuits design for thousand core processors Parallel symbolic analysis of large analog circuits on GPU platforms Algorithms for CAD tools VLSI design A multilevel memetic algorithm for large SAT encoded problems etc **Entwurf integrierter 3D-Systeme der Elektronik** Jens Lienig, Manfred Dietrich, 2012-09-14 Der

zunehmende Einsatz dreidimensionaler strukturierter elektronischer Schaltkreise und Baugruppen ermöglicht eine signifikante Steigerung ihrer Funktionalität durch hohe Integrationsdichten sowie heterogener Integration Dieser heute zu beobachtende technologische Paradigmenwechsel hat einen gravierenden Einfluss auf die Vorgehensweise beim Entwurf der neuartigen Baugruppen Das Buch stellt die sich ergebenden Herausforderungen vor und präsentiert neuartige Lösungen Nach einer Einführung in 3D Systeme Teil I mit den sich ergebenden neuen Anwendungsmöglichkeiten wird detailliert auf die beiden wesentlichen Abschnitte des Entwurfs Modellierung und Simulation Teil II sowie Layoutentwurf Teil III eingegangen Die Behandlung der Probleme mit zugehörigen Lösungen erfolgt dabei entsprechend des Entwurfsflusses d.h. in der Reihenfolge ihrer Bearbeitung Das Buch ist interessant insbesondere für Entscheider in der Industrie sowie für Studenten der Elektronik an Universitäten und Fachhochschulen **Design of 3D Integrated Circuits and Systems** Rohit

Sharma, 2014-11-12 Three dimensional 3D integration of microsystems and subsystems has become essential to the future of semiconductor technology development 3D integration requires a greater understanding of several interconnected systems stacked over each other While this vertical growth profoundly increases the system functionality it also exponentially increases the design complexity Design of 3D Integrated Circuits and Systems tackles all aspects of 3D integration including 3D circuit and system design new processes and simulation techniques alternative communication schemes for 3D circuits and systems application of novel materials for 3D systems and the thermal challenges to restrict power dissipation and improve performance of 3D systems Containing contributions from experts in industry as well as academia this authoritative text Illustrates different 3D integration approaches such as die to die die to wafer and wafer to wafer Discusses the use of interposer technology and the role of Through Silicon Vias TSVs Presents the latest improvements in three major fields of thermal management for multiprocessor systems on chip MPSoCs Explores ThruChip Interface TCI NAND flash memory stacking and emerging applications Describes large scale integration testing and state of the art low power testing solutions Complete with experimental results of chip level 3D integration schemes tested at IBM and case studies on advanced

complementary metal oxide semiconductor CMOS integration for 3D integrated circuits ICs Design of 3D Integrated Circuits and Systems is a practical reference that not only covers a wealth of design issues encountered in 3D integration but also demonstrates their impact on the efficiency of 3D systems

Design Exploration for Three-dimensional Integrated Circuits (3DICs) Xiaoxia Wu,2010

Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals [EDA for IC System Design, Verification, and Testing](#) Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set *Three-Dimensional Integrated Circuit Layout* Andrew Harter,1991-11-28 First published in 1991 this thesis concentrates upon the design of three dimensional rather than the traditional two dimensional circuits The theory behind such circuits is presented in detail together with experimental results

Three-Dimensional Design Methodologies for Tree-based FPGA Architecture Vinod Pangracious,Zied Marrakchi,Habib Mehrez,2015-06-25 This book focuses on the development of 3D design and implementation methodologies for Tree based FPGA architecture It also stresses the needs for new and augmented 3D CAD tools to support designs such as the design for 3D to manufacture high performance 3D integrated circuits and

reconfigurable FPGA based systems This book was written as a text that covers the foundations of 3D integrated system design and FPGA architecture design It was written for the use in an elective or core course at the graduate level in field of Electrical Engineering Computer Engineering and Doctoral Research programs No previous background on 3D integration is required nevertheless fundamental understanding of 2D CMOS VLSI design is required It is assumed that reader has taken the core curriculum in Electrical Engineering or Computer Engineering with courses like CMOS VLSI design Digital System Design and Microelectronics Circuits being the most important It is accessible for self study by both senior students and professionals alike *Science Abstracts* ,1993

Whispering the Techniques of Language: An Psychological Journey through **Three Dimensional Integrated Circuit Design Eda Design And Microarchitectures Integrated Circuits And Systems**

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