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Towards a Design Flow for Reversible Logic



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Towards A Design Flow For Reversible Logic

Rupak Majumdar, Viktor Kunčák



Towards A Design Flow For Reversible Logic:

Towards a Design Flow for Reversible Logic Robert Wille, Rolf Drechsler, 2010-07-28 The development of computing machines found great success in the last decades But the ongoing miniaturization of integrated circuits will reach its limits in the near future Shrinking transistor sizes and power dissipation are the major barriers in the development of smaller and more powerful circuits Reversible logic provides an alternative that may overcome many of these problems in the future For low power design reversible logic offers significant advantages since zero power dissipation will only be possible if computation is reversible Furthermore quantum computation profits from enhancements in this area because every quantum circuit is inherently reversible and thus requires reversible descriptions However since reversible logic is subject to certain restrictions e.g fanout and feedback are not directly allowed the design of reversible circuits significantly differs from the design of traditional circuits Nearly all steps in the design flow like synthesis verification or debugging must be redeveloped so that they become applicable to reversible circuits as well But research in reversible logic is still at the beginning No continuous design flow exists so far In this book contributions to a design flow for reversible logic are represented This includes advanced methods for synthesis optimization verification and debugging [Reversible Logic Synthesis Methodologies with Application to Quantum Computing](#) Saleem Mohammed Ridha Taha, 2015-09-24 This book opens the door to a new interesting and ambitious world of reversible and quantum computing research It presents the state of the art required to travel around that world safely Top world universities companies and government institutions are in a race of developing new methodologies algorithms and circuits on reversible logic quantum logic reversible and quantum computing and nano technologies In this book twelve reversible logic synthesis methodologies are presented for the first time in a single literature with some new proposals Also the sequential reversible logic circuitries are discussed for the first time in a book Reversible logic plays an important role in quantum computing Any progress in the domain of reversible logic can be directly applied to quantum logic One of the goals of this book is to show the application of reversible logic in quantum computing A new implementation of wavelet and multiwavelet transforms using quantum computing is performed for this purpose Researchers in academia or industry and graduate students who work in logic synthesis quantum computing nano technology and low power VLSI circuit design will be interested in this book *Proceedings of the International Conference on Computational Intelligence and Sustainable Technologies* Kedar Nath Das, Debasish Das, Anjan Kumar Ray, Ponnuthurai Nagarathnam Suganthan, 2022-02-12 This book presents the collection of the accepted research papers presented in the 1st International Conference on Computational Intelligence and Sustainable Technologies ICoCIST 2021 This edited book contains the articles related to the themes on artificial intelligence in machine learning big data analysis soft computing techniques pattern recognitions sustainable infrastructural development sustainable grid computing and innovative technology for societal development renewable energy and innovations in Internet of Things IoT [Reversible Computation](#) Alexis De Vos, Robert

Wille,2012-04-13 This book constitutes the thoroughly refereed post conference proceedings of the 7th International Reversible Computation RC 2011 held in Gent Belgium in July 2011 The 10 revised full papers presented were carefully reviewed and selected from 25 initial submissions for inclusion in the book The papers are devoted to all aspects of reversible computation ranging from theoretical and experimental aspects to various applications Topics addressed are functional language for reversible computations logic design reversible circuits designed by a software toolkit called RevKit application of reversible computation to the domain of quantum circuits and physical realizations of reversible circuits in CMOS technologies

Proceeding of Fifth International Conference on Microelectronics, Computing and Communication Systems

Vijay Nath,J. K. Mandal,2021-09-09 This book presents high quality papers from the Fifth International Conference on Microelectronics Computing Communication Systems MCCS 2020 It discusses the latest technological trends and advances in MEMS and nanoelectronics wireless communication optical communication instrumentation signal processing image processing bioengineering green energy hybrid vehicles environmental science weather forecasting cloud computing renewable energy RFID CMOS sensors actuators transducers telemetry systems embedded systems and sensor network applications It includes papers based on original theoretical practical and experimental simulations development applications measurements and testing The applications and solutions discussed here provide excellent reference material for future product development

New Data Structures and Algorithms for Logic Synthesis and Verification Luca Gaetano Amaru,2016-08-02 This book introduces new logic primitives for electronic design automation tools The author approaches fundamental EDA problems from a different unconventional perspective in order to demonstrate the key role of rethinking EDA solutions in overcoming technological limitations of present and future technologies The author discusses techniques that improve the efficiency of logic representation manipulation and optimization tasks by taking advantage of majority and biconditional logic primitives Readers will be enabled to accelerate formal methods by studying core properties of logic circuits and developing new frameworks for logic reasoning engines

Reversible Computation Iain Phillips,Hafizur Rahaman,2017-06-26 This book constitutes the refereed proceedings of the 9th International Conference on Reversible Computation RC 2017 held in Kolkata India in July 2017 The 13 full and 5 short papers included in this volume together with one invited paper were carefully reviewed and selected from 47 submissions The papers are organized in the following topical sections foundations reversible circuit synthesis reversible circuit optimization testing and fault tolerance and quantum circuits

Theory of Reversible Computing Kenichi Morita,2017-11-06 This book describes reversible computing from the standpoint of the theory of automata and computing It investigates how reversibility can be effectively utilized in computing A reversible computing system is a backward deterministic system such that every state of the system has at most one predecessor Although its definition is very simple it is closely related to physical reversibility one of the fundamental microscopic laws of Nature Authored by the leading

scientist on the subject this book serves as a valuable reference work for anyone working in reversible computation or in automata theory in general This work deals with various reversible computing models at several different levels which range from the microscopic to the macroscopic and aims to clarify how computation can be carried out efficiently and elegantly in these reversible computing models Because the construction methods are often unique and different from those in the traditional methods these computing models as well as the design methods provide new insights for future computing systems Organized bottom up the book starts with the lowest scale of reversible logic elements and circuits made from them This is followed by reversible Turing machines the most basic computationally universal machines and some other types of reversible automata such as reversible multi head automata and reversible counter machines The text concludes with reversible cellular automata for massively parallel spatiotemporal computation In order to help the reader have a clear understanding of each model the presentations of all different models follow a similar pattern the model is given in full detail a short informal discussion is held on the role of different elements of the model and an example with illustrations follows each model

Logic, Language, Information, and Computation Jouko Väänänen, Åsa Hirvonen, Ruy de Queiroz, 2016-08-05 Edited in collaboration with FoLLI the Association of Logic Language and Information this book constitutes the refereed proceedings of the 23rd Workshop on Logic Language Information and Communication WoLLIC 2016 held in Puebla Mexico in August 2016 The 23 contributed papers presented together with 9 invited lectures and tutorials were carefully reviewed and selected from 33 submissions The focus of the workshop is to provide a forum on inter disciplinary research involving formal logic computing and programming theory and natural language and reasoning

Reversible Computation Robert Glück, Tetsuo Yokoyama, 2013-01-16 This book constitutes the refereed proceedings of the 4th International Workshop on Reversible Computation RC 2012 held in Copenhagen Denmark in July 2012 The 19 contributions presented in this volume were carefully reviewed and selected from 46 submissions The papers cover theoretical considerations reversible software and reversible hardware and physical realizations and applications in quantum computing

Advanced Computing and Systems for Security Rituparna Chaki, Agostino Cortesi, Khalid Saeed, Nabendu Chaki, 2019-08-14 This book features extended versions of selected papers that were presented and discussed at the 6th International Doctoral Symposium on Applied Computation and Security Systems ACSS 2019 held in Kolkata India on 12 13 March 2019 Organized by the Departments of Computer Science Engineering and A K Choudhury School of Information Technology both from the University of Calcutta the symposium's international partners were Ca Foscari University of Venice Italy and Bialystok University of Technology Poland The chapters cover topics such as biometrics image processing pattern recognition algorithms cloud computing wireless sensor networks and security systems reflecting the various symposium sessions

Synthesis of Quantum Circuits vs. Synthesis of Classical Reversible Circuits Alexis De Vos, Stijn De Baerdemacker, Yvan Van Rentergem, 2018-07-03 At first sight quantum computing is completely different from classical computing Nevertheless a link is provided by reversible

computation Whereas an arbitrary quantum circuit acting on qubits is described by an unitary matrix with 2 a reversible classical circuit acting on bits is described by a 2 2 permutation matrix The permutation matrices are studied in group theory of finite groups in particular the symmetric group the unitary matrices are discussed in group theory of continuous groups a k a Lie groups in particular the unitary group U Both the synthesis of a reversible logic circuit and the synthesis of a quantum logic circuit take advantage of the decomposition of a matrix the former of a permutation matrix the latter of a unitary matrix In both cases the decomposition is into three matrices In both cases the decomposition is not unique

Principles of Big Graph: In-depth Insight ,2023-01-24 Principles of Big Graph In depth Insight Volume 128 in the Advances in Computer series highlights new advances in the field with this new volume presenting interesting chapters on a variety of topics including CESDAM Centered subgraph data matrix for large graph representation Bivariate cluster and suitability analysis of NoSQL Solutions for big graph applications An empirical investigation on Big Graph using deep learning Analyzing correlation between quality and accuracy of graph clustering geneBF Filtering protein coded gene graph data using bloom filter Processing large graphs with an alternative representation MapReduce based convolutional graph neural networks A comprehensive review Fast exact triangle counting in large graphs using SIMD acceleration A comprehensive investigation on attack graphs Qubit representation of a binary tree and its operations in quantum computation Modified ML KNN Role of similarity measures and nearest neighbor configuration in multi label text classification on big social network graph data Big graph based online learning through social networks Community detection in large scale real world networks Power rank An interactive web page ranking algorithm GA based energy efficient modelling of a wireless sensor network The major challenges of big graph and their solutions A review and An investigation on socio cyber crime graph Provides an update on the issues and challenges faced by current researchers Updates on future research agendas Includes advanced topics for intensive research for researchers [Recent Trends in Intelligent Systems and Next Generation Wireless Communication](#) Santos Kumar Das,Santanu Kumar Behera,2025-08-19 This book is a collection of cutting edge research papers from the First National Conference on Intelligent Systems IoT and Wireless Communication for Society IIWCS 2024 It features contributions from researchers and practitioners worldwide sharing the latest findings in the fields of intelligent systems the Internet of Things IoT and wireless communication The volume covers novel techniques and methodologies in areas such as machine learning 5G and beyond communication RFID MEMS and VLSI engineering The proceedings are a valuable resource for researchers practitioners and students interested in the latest developments in intelligent systems IoT and wireless communication *Implementation and Application of Functional Languages* Andy Gill,Jurriaan Hage,2012-11-06 This book constitutes the thoroughly refereed post conference proceedings of the 23rd International Symposium on Implementation and Application of Functional Languages IFL 2011 held in Lawrence Kansas USA in October 2011 The 11 revised full papers presented were carefully reviewed and selected from 33 submissions The papers by researchers and practitioners who are

actively engaged in the implementation and the use of functional and function based programming languages describe practical and theoretical work as well as applications and tools They discuss new ideas and concepts as well as work in progress and results *Emerging Research in Electronics, Computer Science and Technology* V Sridhar,Holalu Seenappa Sheshadri,M C Padma,2013-09-13 PES College of Engineering is organizing an International Conference on Emerging Research in Electronics Computer Science and Technology ICERECT 12 in Mandya and merging the event with Golden Jubilee of the Institute The Proceedings of the Conference presents high quality peer reviewed articles from the field of Electronics Computer Science and Technology The book is a compilation of research papers from the cutting edge technologies and it is targeted towards the scientific community actively involved in research activities *Reversible Computation* Jean Krivine,Jean-Bernard Stefani,2015-06-19 This book constitutes the refereed proceedings of the 7th International Conference on Reversible Computation RC 2015 held in Grenoble France in July 2015 The 19 papers presented together with 1 invited talk were carefully reviewed and selected from 30 submissions The Conference on Reversible Computation particularly includes the following topics reversible machines reversible languages design and verification of quantum circuits design of reversible circuits and circuit synthesis *Reversibility and Universality* Andrew Adamatzky,2018-02-26 This book is a tribute to Kenichi Morita s ideas and achievements in theoretical computer science reversibility and computationally universal mathematical machines It offers a unique source of information on universality and reversibility in computation and is an indispensable book for computer scientists mathematicians physicists and engineers Morita is renowned for his works on two dimensional language accepting automata complexity of Turing machines universality of cellular automata regular and context free array grammars and undecidability His high impact works include findings on parallel generation and parsing of array languages by means of reversible automata construction of a reversible automaton from Fredkin gates solving a firing squad synchronization problem in reversible cellular automata self reproduction in reversible cellular spaces universal reversible two counter machines solution of nondeterministic polynomial NP problems in hyperbolic cellular automata reversible P systems a new universal reversible logic element with memory and reversibility in asynchronous cellular automata Kenichi Morita s achievements in reversibility universality and theory of computation are celebrated in over twenty high profile contributions from his colleagues collaborators students and friends The theoretical constructs presented in this book are amazing in their diversity and depth of intellectual insight addressing queue automata hyperbolic cellular automata Abelian invertible automata number conserving cellular automata Brownian circuits chemical automata logical gates implemented via glider collisions computation in swarm networks picture arrays universal reversible counter machines input position restricted models of language acceptors descriptive complexity and persistence of cellular automata partitioned cellular automata firing squad synchronization algorithms reversible asynchronous automata reversible simulations of ranking trees Shor s factorization algorithms and power consumption of

cellular automata **CMOSET 2011 Final Program** CMOS Emerging Technologies Research,2014-08-28 Final program for the CMOSET 2011 conference *Computer Aided Verification* Rupak Majumdar,Viktor Kunčak,2017-07-11 The two volume set LNCS 10426 and LNCS 10427 constitutes the refereed proceedings of the 29th International Conference on Computer Aided Verification CAV 2017 held in Heidelberg Germany in July 2017 The total of 50 full and 7 short papers presented together with 5 keynotes and tutorials in the proceedings was carefully reviewed and selected from 191 submissions The CAV conference series is dedicated to the advancement of the theory and practice of computer aided formal analysis of hardware and software systems The conference covers the spectrum from theoretical results to concrete applications with an emphasis on practical verification tools and the algorithms and techniques that are needed for their implementation

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