

The Software Optimization Cookbook

High-Performance Recipes for IA-32 Platforms

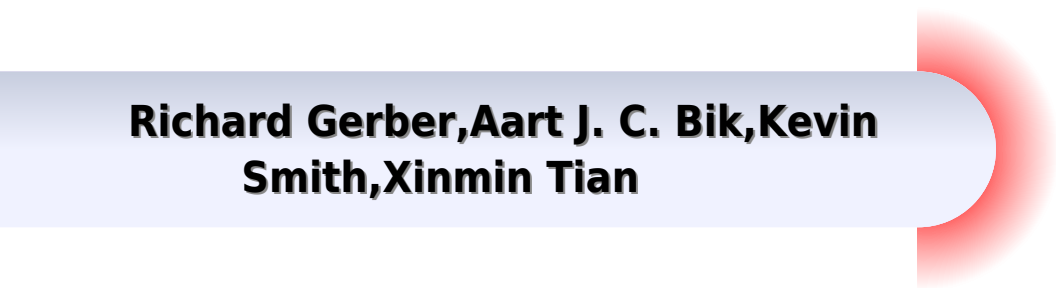
Richard Gerber, Aart J.C. Bik, Kevin B. Smith, and Xinmin Tian

Second Edition



The Software Optimization Cookbook High Performance Recipes For Ia 32 Platforms 2nd Edition

**Richard Gerber,Aart J. C. Bik,Kevin
Smith,Xinmin Tian**



The Software Optimization Cookbook High Performance Recipes For Ia 32 Platforms 2nd Edition:

The Software Optimization Cookbook ,2006 **The Software Optimization Cookbook** Richard Gerber,Aart J. C.

Bik, Kevin Smith, Xinmin Tian, 2006 Annotation Four Intel experts explain the techniques and tools that you can use to improve the performance of applications for IA 32 processors Simple explanations and code examples help you to develop software that benefits from Intel Extended Memory 64 Technology Intel EM64T multi core processing Hyper Threading Technology OpenMP and multimedia extensions This book guides you through the growing collection of software tools compiler switches and coding optimizations showing you efficient ways to get the best performance from software applications **Embedded**

Computing for High Performance João Manuel Paiva Cardoso, José Gabriel de Figueiredo Coutinho, Pedro C.

Diniz, 2017-06-13 Embedded Computing for High Performance Design Exploration and Customization Using High level Compilation and Synthesis Tools provides a set of real life example implementations that migrate traditional desktop systems to embedded systems Working with popular hardware including Xilinx and ARM the book offers a comprehensive description of techniques for mapping computations expressed in programming languages such as C or MATLAB to high performance embedded architectures consisting of multiple CPUs GPUs and reconfigurable hardware FPGAs The authors demonstrate a domain specific language LARA that facilitates retargeting to multiple computing systems using the same source code In this way users can decouple original application code from transformed code and enhance productivity and program portability After reading this book engineers will understand the processes methodologies and best practices needed for the development of applications for high performance embedded computing systems Focuses on maximizing performance while managing energy consumption in embedded systems Explains how to retarget code for heterogeneous systems with GPUs and FPGAs Demonstrates a domain specific language that facilitates migrating and retargeting existing applications to modern systems Includes downloadable slides tools and tutorials Parallel Programming with Intel Parallel Studio XE

Stephen Blair-Chappell, Andrew Stokes, 2012-04-19 Optimize code for multi core processors with Intel s Parallel Studio Parallel programming is rapidly becoming a must know skill for developers Yet where to start This teach yourself tutorial is an ideal starting point for developers who already know Windows C and C and are eager to add parallelism to their code With a focus on applying tools techniques and language extensions to implement parallelism this essential resource teaches you how to write programs for multicore and leverage the power of multicore in your programs Sharing hands on case studies and real world examples the authors examine the challenges of each project and show you how to overcome them Explores conversion of serial code to parallel Focuses on implementing Intel Parallel Studio Highlights the benefits of using parallel code Addresses error and performance optimization of code Includes real world scenarios that illustrate the techniques of advanced parallel programming situations Parallel Programming with Intel Parallel Studio dispels any concerns of difficulty and gets you started creating faster code with Intel Parallel Studio *Power and Performance* Jim Kukunas, 2015-04-27

Power and Performance Software Analysis and Optimization is a guide to solving performance problems in modern Linux systems. Power efficient chips are no help if the software those chips run on is inefficient. Starting with the necessary architectural background as a foundation, the book demonstrates the proper usage of performance analysis tools in order to pinpoint the cause of performance problems and includes best practices for handling common performance issues. Those tools identify. Provides expert perspective from a key member of Intel's optimization team on how processors and memory systems influence performance. Presents ideas to improve architectures running mobile desktop or enterprise platforms. Demonstrates best practices for designing experiments and benchmarking throughout the software lifecycle. Explains the importance of profiling and measurement to determine the source of performance issues. *Embedded System Design* Peter

Marwedel, 2010-11-16. Until the late 1980s, information processing was associated with large mainframe computers and huge tape drives. During the 1990s, this trend shifted toward information processing with personal computers or PCs. The trend toward miniaturization continues, and in the future, the majority of information processing systems will be small mobile computers, many of which will be embedded into larger products and interfaced to the physical environment. Hence, these kinds of systems are called embedded systems. Embedded systems together with their physical environment are called cyber physical systems. Examples include systems such as transportation and fabrication equipment. It is expected that the total market volume of embedded systems will be significantly larger than that of traditional information processing systems such as PCs and mainframes. Embedded systems share a number of common characteristics. For example, they must be dependable, efficient, meet real time constraints, and require customized user interfaces instead of generic keyboard and mouse interfaces. Therefore, it makes sense to consider common principles of embedded system design. *Embedded System Design* starts with an introduction into the area and a survey of specification models and languages for embedded and cyber physical systems. It provides a brief overview of hardware devices used for such systems and presents the essentials of system software for embedded systems like real time operating systems. The book also discusses evaluation and validation techniques for embedded systems. Furthermore, the book presents an overview of techniques for mapping applications to execution platforms. Due to the importance of resource efficiency, the book also contains a selected set of optimization techniques for embedded systems, including special compilation techniques. The book closes with a brief survey on testing. *Embedded System Design* can be used as a text book for courses on embedded systems and as a source which provides pointers to relevant material in the area for PhD students and teachers. It assumes a basic knowledge of information processing hardware and software. Courseware related to this book is available at <http://ls12-www.cs.tu-dortmund.de/marwedel>

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