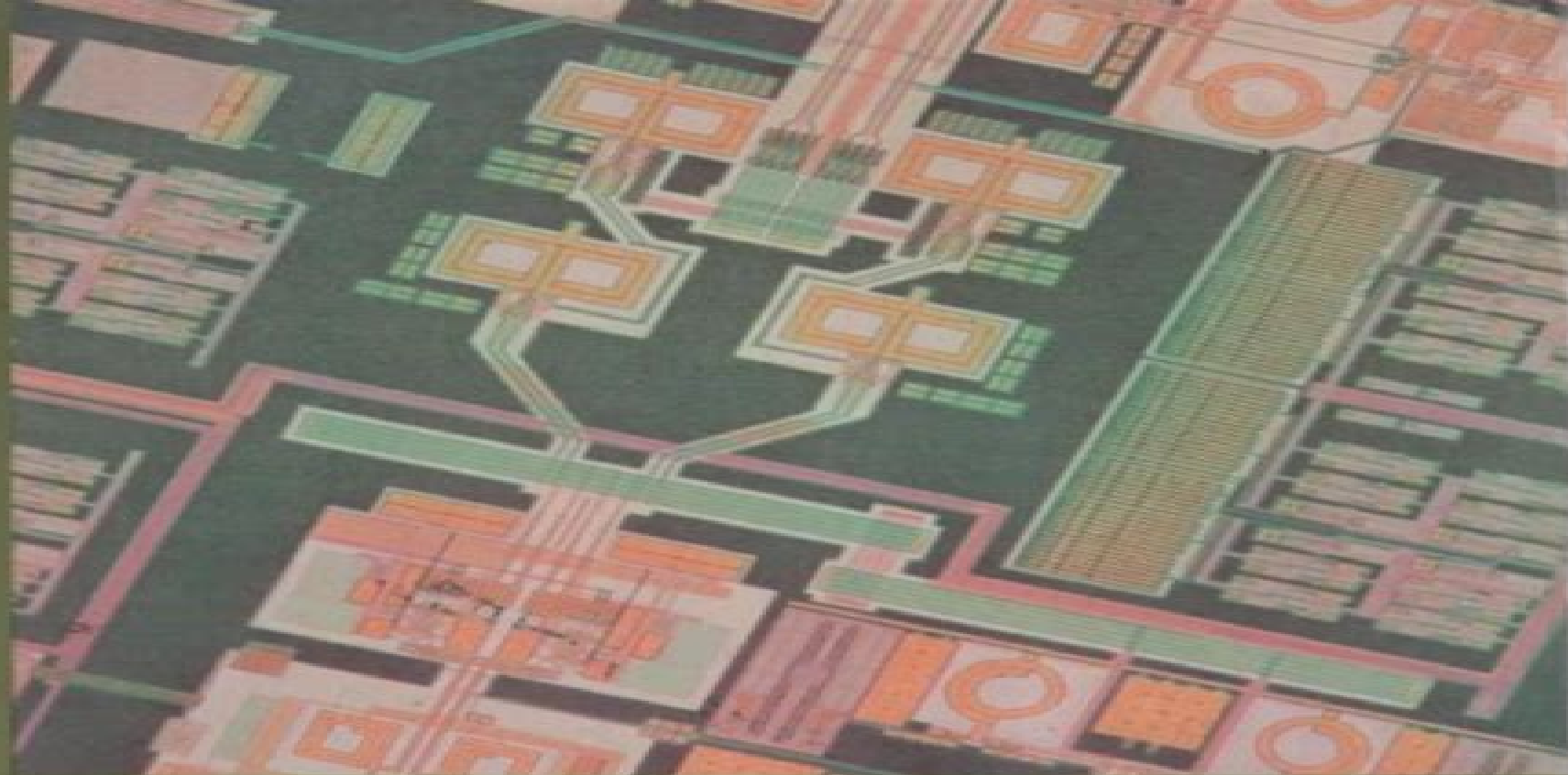




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The Design of CMOS Radio-Frequency Integrated Circuits

Second Edition

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The Design of CMOS Radio-Frequency Integrated Circuits Thomas H. Lee, 2003-12-22 This book first published in 2004 is an expanded and thoroughly revised edition of Tom Lee's acclaimed guide to the design of gigahertz RF integrated circuits. A new chapter on the principles of wireless systems provides a bridge between system and circuit issues. The chapters on low noise amplifiers, oscillators, and phase noise have been significantly expanded. The chapter on architectures now contains several examples of complete chip designs, including a GPS receiver and a wireless LAN transceiver that bring together the theoretical and practical elements involved in producing a prototype chip. Every section has been revised and updated with findings in the field, and the book is packed with physical insights and design tips and includes a historical overview that sets the whole field in context. With hundreds of circuit diagrams and homework problems, this is an ideal textbook for students taking courses on RF design and a valuable reference for practising engineers. **Computer-Aided**

Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck. Ten years ago analog seemed to be a dead end technology. Today System on Chip (SoC) designs are increasingly mixed signal designs. With the advent of application specific integrated circuits (ASIC) technologies that can integrate both analog and digital functions on a single chip, analog has become more crucial than ever to the design process. Today designers are moving beyond hand-crafted one-transistor-at-a-time methods. They are using new circuit and physical synthesis tools to design practical analog circuits, new modeling and analysis tools to allow rapid exploration of system level alternatives, and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago. To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field, this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools. Areas covered are: Analog synthesis, Symbolic analysis, Analog layout, Analog modeling and analysis, Specialized analog simulation, Circuit centering and yield optimization, Circuit testing. **Computer Aided Design of Analog Integrated Circuits and Systems** is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck. **Subject Guide to Books in Print**, 1991

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Books in Print Supplement ,2002

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The Design and Implementation of

Low-Power CMOS Radio Receivers Derek Shaeffer, Thomas H Lee, 2013-03-21 It is hardly a profound observation to note that we remain in the midst of a wireless revolution In 1998 alone over 150 million cell phones were sold worldwide representing an astonishing 50% increase over the previous year Maintaining such a remarkable growth rate requires constant innovation to decrease cost while increasing performance and functionality Traditionally wireless products have depended on a mixture of semiconductor technologies spanning GaAs bipolar and BiCMOS just to name a few A question that has been hotly debated is whether CMOS could ever be suitable for RF applications However given the acknowledged inferiority of CMOS transistors relative to those in other candidate technologies it has been argued by many that CMOS RF is an oxymoron an endeavor best left cloistered in the ivory towers of academia In rebuttal there are several compelling reasons to consider CMOS for wireless applications Aside from the exponential device and density improvements delivered regularly by Moore's law only CMOS offers a technology path for integrating RF and digital elements potentially leading to exceptionally compact and low cost devices To enable this achievement several thorny issues need to be resolved Among these are the problem of poor passive components broadband noise in MOSFETs and phase noise in oscillators made with CMOS Beyond the component level there is also the important question of whether there are different architectural choices that one would make if CMOS were used given the different constraints

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Design of CMOS RF Integrated Circuits and Systems Kiat Seng Yeo, Manh Anh Do, Chirn Chye Boon, 2010 This book provides the most comprehensive and in depth coverage of the latest circuit design developments in RF CMOS technology It is a practical and cutting edge guide packed with proven circuit techniques and innovative design methodologies for solving challenging problems associated with RF integrated circuits and systems This invaluable resource features a collection of the finest design practices that may soon drive the system on chip revolution Using this book's state of the art design techniques one can apply existing technologies in novel ways and to create new circuit designs for the future

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