

VLSI DESIGN LABORATORY MANUAL



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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
VASIREDDY VENKATADRI INSTITUTE OF TECHNOLOGY::NAMBUR
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Vlsi Lab Manual

**Kohei Arai, Supriya Kapoor, Rahul
Bhatia**



Vlsi Lab Manual:

Lab Manual Latest Edition Dr. J. P. Goel, 2016-12-17 Lab E Manual Physics For XIIth Practicals A Every student will perform 10 experiments 5 from each section 8 activities 4 from each section during the academic year Two demonstration experiments must be performed by the teacher with participation of students The students will maintain a record of these demonstration experiments B Evaluation Scheme for Practical Examination One experiment from any one section 8 Marks Two activities one from each section 4 4 8 Marks Practical record experiments activities 6 Marks Record of demonstration experiments Viva based on these experiments 3 Marks Viva on experiments activities 5 Marks Total 30 Marks Section A Experiments 1 To determine resistance per cm of a given wire by plotting a graph of potential difference versus current 2 To find resistance of a given wire using metre bridge and hence determine the specific resistance of its material 3 To verify the laws of combination series parallel of resistances using a metre bridge 4 To compare the emf of two given primary cells using potentiometer 5 To determine the internal resistance of given primary cells using potentiometer 6 To determine resistance of a galvanometer by half deflection method and to find its figure of merit 7 To convert the given galvanometer of known resistance and figure of merit into an ammeter and voltmeter of desired range and to verify the same 8 To find the frequency of the a c mains with a sonometer Activities 1 To measure the resistance and impedance of an inductor with or without iron core 2 To measure resistance voltage AC DC current AC and check continuity of a given circuit using multimeter 3 To assemble a household circuit comprising three bulbs three on off switches a fuse and a power source 4 To assemble the components of a given electrical circuit 5 To study the variation in potential drop with length of a wire for a steady current 6 To draw the diagram of a given open circuit comprising at least a battery resistor rheostat key ammeter and voltmeter Mark the components that are not connected in proper order and correct the circuit and also the circuit diagram Section B Experiments 1 To find the value of v for different values of u in case of a concave mirror and to find the focal length 2 To find the focal length of a convex lens by plotting graphs between u and v or between $1/u$ and $1/v$ 3 To find the focal length of a convex mirror using a convex lens 4 To find the focal length of a concave lens using a convex lens 5 To determine angle of minimum deviation for a given prism by plotting a graph between angle of incidence and angle of deviation 6 To determine refractive index of a glass slab using a travelling microscope 7 To find refractive index of a liquid by using i concave mirror ii convex lens and plane mirror 8 To draw the I V characteristic curve of a p n junction in forward bias and reverse bias 9 To draw the characteristic curve of a zener diode and to determine its reverse break down voltage 10 To study the characteristics of a common emitter npn or pnp transistor and to find out the values of current and voltage gains Activitie 1 To study effect of intensity of light by varying distance of the source on a L D R 2 To identify a diode a LED a transistor and IC a resistor and a capacitor from mixed collection of such items 3 Use of multimeter to i identify base of transistor ii distinguish between npn and pnp type transistors iii see the unidirectional flow of current in case of a diode and a LED iv

check whether a given electronic component e g diode transistor or I C is in working order 4 To observe refraction and lateral deviation of a beam of light incident obliquely on a glass slab 5 To observe polarization of liquid using two Polaroids 6 To observe diffraction of light due to a thin slit 7 To study the nature and size of the image formed by i convex lens ii concave mirror on a screen by using a candle and a screen for different distances of the candle from the lens mirror 8 To obtain a lens combination with the specified focal length by using two lenses from the given set of lenses Suggested Investigatory Projects 1 To investigate whether the energy of a simple pendulum is conserved 2 To determine the radius of gyration about the centre of mass of a metre scale as a bar pendulum 3 To investigate changes in the velocity of a body under the action of a constant force and determine its acceleration 4 To compare effectiveness of different materials as insulators of heat 5 To determine the wavelengths of laser beam by diffraction 6 To study various factors on which the internal resistance emf of a cell depends 7 To construct a time switch and study dependence of its time constant on various factors 8 To study infrared radiations emitted by different sources using photo transistor 9 To compare effectiveness of different materials as absorbers of sound 10 To design an automatic traffic signal system using suitable combination of logic gates 11 To study luminosity of various electric lamps of different powers and make 12 To compare the Young s modulus of elasticity of different specimens of rubber and also draw their elastic hysteresis curve 13 To study collision of two balls in two dimensions 14 To study frequency response of i a resistor an inductor and a capacitor ii RL circuit iii RC circuit iv LCR series circuit

Microprocessor (8085) Lab Manual G.T. Swamy,2006 **Cyber-Physical Laboratories in Engineering and Science Education** Michael E. Auer,Abul K.M. Azad,Arthur Edwards,Ton de Jong,2018-04-26 This volume investigates a number of issues needed to develop a modular effective versatile cost effective pedagogically embedded user friendly and sustainable online laboratory system that can deliver its true potential in the national and global arenas This allows individual researchers to develop their own modular systems with a level of creativity and innovation while at the same time ensuring continuing growth by separating the responsibility for creating online laboratories from the responsibility for overseeing the students who use them The volume first introduces the reader to several system architectures that have proven successful in many online laboratory settings The following chapters then describe real life experiences in the area of online laboratories from both technological and educational points of view The volume further collects experiences and evidence on the effective use of online labs in the context of a diversity of pedagogical issues It also illustrates successful online laboratories to highlight best practices as case studies and describes the technological design strategies implementation details and classroom activities as well as learning from these developments Finally the volume describes the creation and deployment of commercial products tools and services for online laboratory development It also provides an idea about the developments that are on the horizon to support this area *Proceedings* ,1990 **With Honors** Grace Wen, Success in life takes more than straight A s Melinda is an honors engineering student with a secret she has cheated on every lab assignment since her

junior year As graduation day approaches she realizes she can't keep trying to be someone she's not Can she find a way to live her truth This story was previously published in 2012 *Microelectronics Education* B. Courtois, N. Guillemot, G. Kamarinos, G. Stéhelin, 2013-03-09 This is the third edition of the European Workshop on Microelectronics Education EWME A steady state regime has now been reached An international community of university teachers is constituted they exchange their experience and their pedagogical tools They discuss the best ways to transfer the rapidly changing techniques to their students and to introduce them to the new physical and mathematical concepts and models for the innovative techniques devices circuits and design methods The number of abstracts submitted to EWME 2000 about one hundred enabled the scientific committee to proceed to a clear selection EWME is a European meeting Indeed authors from 20 different European countries contribute to this volume Nevertheless the participation of authors from Brazil Canada China New Zealand and USA shows that the workshop gradually attains an international dimension The 20th century can be characterized as the century of electron The electron as an elementary particle was discovered by J J Thomson in 1897 and was rapidly used to transfer energy and information Thanks to electron universe and micro cosmos could be explored Electron became the omnipotent and omnipresent almost immaterial angel of our World This was made possible thanks to electronics and for the last 30 years to microelectronics Microelectronics not only modified and even radically transformed the industrial and every day landscapes but it also led to the so called information revolution with which begins the 21st century **IEEE Circuits & Devices**, 2000 **Circuit Analysis (for Anna University)** Gnanasivam, 2017 *MOSFET Modeling & BSIM3 User's Guide* Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i.e. I-V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device

modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike **Technical Reports Awareness Circular : TRAC.** ,1989-04 *Semiconductor Manufacturing Technology Workshop* ,1994 Parallel Computing Works! Geoffrey C. Fox,Roy D. Williams,Guiseppe C. Messina,2014-06-28 A clear illustration of how parallel computers can be successfully applied to large scale scientific computations This book demonstrates how a variety of applications in physics biology mathematics and other sciences were implemented on real parallel computers to produce new scientific results It investigates issues of fine grained parallelism relevant for future supercomputers with particular emphasis on hypercube architecture The authors describe how they used an experimental approach to configure different massively parallel machines design and implement basic system software and develop algorithms for frequently used mathematical computations They also devise performance models measure the performance characteristics of several computers and create a high performance computing facility based exclusively on parallel computers By addressing all issues involved in scientific problem solving *Parallel Computing Works* provides valuable insight into computational science for large scale parallel architectures For those in the sciences the findings reveal the usefulness of an important experimental tool Anyone in supercomputing and related computational fields will gain a new perspective on the potential contributions of parallelism Includes over 30 full color illustrations *Proceedings American Society for Engineering Education. Conference*,1994 *Scientific and Technical Aerospace Reports* ,1995 **Readings in Hardware/Software Co-Design** Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s *Energy Research Abstracts* ,1993 Advances in Information and Communication Networks Kohei Arai,Supriya Kapoor,Rahul Bhatia,2018-12-05 The book gathering the proceedings of the Future of Information and Communication Conference FICC 2018 is a remarkable collection of chapters covering a wide range of topics in areas of information and communication technologies and their applications to the real world It includes 104 papers and posters by pioneering academic researchers scientists industrial engineers and students from all around the world which contribute to our understanding of relevant trends of current research on communication data science ambient intelligence networking computing security and Internet of Things This book collects state of the art chapters on all aspects of information science and communication technologies from classical to intelligent and covers both theory and applications of the latest technologies and methodologies Presenting state of the art intelligent methods and techniques for solving real world problems along with a vision of the future research this book is an interesting and useful resource Current Trends in Hardware Verification and Automated Theorem Proving Graham Birtwistle,P.A. Subrahmanyam,2012-12-06 This report describes the partially completed correctness proof of the Viper block model Viper 7 8 9 11 23 is a microprocessor designed

by W J Cullyer C Pygott and J Kershaw at the Royal Signals and Radar Establishment in Malvern England henceforth RSRE for use in safety critical applications such as civil aviation and nuclear power plant control It is currently finding uses in areas such as the deployment of weapons from tactical aircraft To support safety critical applications Viper has a particularly simple design about which it is relatively easy to reason using current techniques and models The designers who deserve much credit for the promotion of formal methods intended from the start that Viper be formally verified Their idea was to model Viper in a sequence of decreasingly abstract levels each of which concentrated on some aspect of the design such as the flow of control the processing of instructions and so on That is each model would be a specification of the next less abstract model and an implementation of the previous model if any The verification effort would then be simplified by being structured according to the sequence of abstraction levels These models or levels of description were characterized by the design team The first two levels and part of the third were written by them in a logical language amenable to reasoning and proof

Dependable Computing - EDCC-2 Andrzej Hlawiczka, Joao G.S. Silva, Luca Simoncini, 1996-09-18 This book constitutes the refereed proceedings of the Second European Dependable Computing Conference EDCC 2 held in Taormina Italy in October 1996 The book presents 26 revised full papers selected from a total of 66 submissions based on the reviews of 146 referees The papers are organized in sections on distributed fault tolerance fault injection modelling and evaluation fault tolerant design basic hardware models testing verification replication and distribution and system level diagnosis

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Table of Contents Vlsi Lab Manual

1. Understanding the eBook Vlsi Lab Manual
 - The Rise of Digital Reading Vlsi Lab Manual
 - Advantages of eBooks Over Traditional Books
2. Identifying Vlsi Lab Manual
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Vlsi Lab Manual
 - User-Friendly Interface
4. Exploring eBook Recommendations from Vlsi Lab Manual
 - Personalized Recommendations

- Vlsi Lab Manual User Reviews and Ratings
- Vlsi Lab Manual and Bestseller Lists
- 5. Accessing Vlsi Lab Manual Free and Paid eBooks
 - Vlsi Lab Manual Public Domain eBooks
 - Vlsi Lab Manual eBook Subscription Services
 - Vlsi Lab Manual Budget-Friendly Options
- 6. Navigating Vlsi Lab Manual eBook Formats
 - ePub, PDF, MOBI, and More
 - Vlsi Lab Manual Compatibility with Devices
 - Vlsi Lab Manual Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Vlsi Lab Manual
 - Highlighting and Note-Taking Vlsi Lab Manual
 - Interactive Elements Vlsi Lab Manual
- 8. Staying Engaged with Vlsi Lab Manual
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Vlsi Lab Manual
- 9. Balancing eBooks and Physical Books Vlsi Lab Manual
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Vlsi Lab Manual
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Vlsi Lab Manual
 - Setting Reading Goals Vlsi Lab Manual
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Vlsi Lab Manual
 - Fact-Checking eBook Content of Vlsi Lab Manual

- Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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