

ISC Silicon PNP Darlington Power Transistor

TIP117

DESCRIPTION

- High DC Current Gain:
 - $\beta_{DC} = 1000(Min) @ I_{C1} = 1A$
- Collector-Emitter Sustaining Voltage:
 - $V_{CE(sus)} = -100V(Min)$
- Low Collector-Emitter Saturation Voltage:
 - $V_{CE(sat)} = -2.0V(Max) @ I_{C1} = 2A$
- Complementary to Type TIP112
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

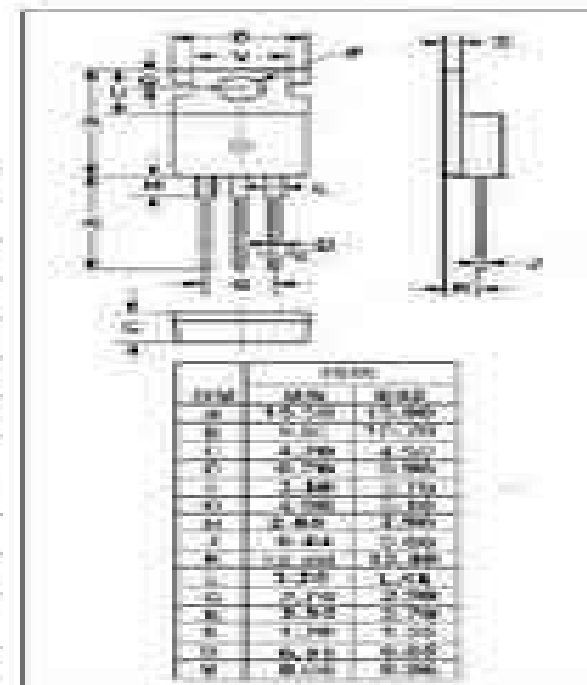
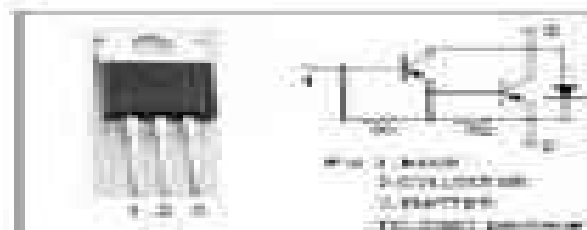
- Designed for general purpose amplifier and low speed switching applications

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CE}	Collector-Base Voltage	-100	V
V_{CE}	Collector-Emitter Voltage	-100	V
V_{EB}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-3	A
I_{CP}	Collector Current-Peak	-4	A
I_B	Base Current	-50	mA
P_D	Collector Power Dissipation T _a =25°C	50	W
	Collector Power Dissipation T _a =25°C	2	
T_J	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-60-150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX.	UNIT
$R_{\theta JA}$	Thermal Resistance, Junction to Case	2.0	°C/W
$R_{\theta SA}$	Thermal Resistance, Junction to Ambient	62.0	°C/W



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