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web although our aim is to give an understanding of latchup as it occurs in cmos switches similar principles apply to many other cmos devices latchup may be defined as the creation of a low impedance path between power supply rails as a result of triggering a parasitic device

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web in electronics a latch up is a type of short circuit which can occur in an integrated circuit ic more specifically it is the inadvertent creation of a low impedance path between the power supply rails of a mosfet circuit triggering a parasitic structure which disrupts proper functioning of the part possibly even leading to

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