x86 Assembly Guide

Contents: Registers | Memory and Addressing | Instructions | Calling Convention

This guide describes the basics of 32-bit x86 assembly language programming, covering a small but useful subset of the available instructions and assembler directives. There are several different assembly languages for generating x86 machine code. The one we will use in CS216 is the Microsoft Macro Assembler (MASM) assembler. MASM uses the standard Intel syntax for writing x86 assembly code.

The full x86 instruction set is large and complex (Intel's x86 instruction set manuals comprise over 2900 pages), and we do not cover it all in this guide. For example, there is a 16-bit subset of the x86 instruction set. Using the 16-bit programming model can be quite complex. It has a segmented memory model, more restrictions on register usage, and so on. In this guide, we will limit our attention to more modern aspects of x86 programming, and delve into the instruction set only in enough detail to get a basic feel for x86 programming.

Resources

- Guide to Using Assembly in Visual Studio a tutorial on building and debugging assembly code in Visual Studio
- Intel x86 Instruction Set (Zack Smith)
- Intel's Pentium Manuals (the full gory details). Includes: Instruction Set Reference (A-M). Instruction Set Reference (N-Z).

Registers

Modern (i.e 386 and beyond) x86 processors have eight 32-bit general purpose registers, as depicted in Figure 1. The register names are mostly historical. For example, EAX used to be called the accumulator since it was used by a number of arithmetic operations, and ECX was known as the counter since it was used to hold a loop index. Whereas most of the registers have lost their special purposes in the modern instruction set, by convention, two are reserved for special purposes — the stack pointer (ESP) and the base pointer (EBP).

For the EAX, EBX, ECX, and EDX registers, subsections may be used. For example, the least significant 2 bytes of EAX can be treated as a 16-bit register called AX. The least significant byte of AX can be used as a single 8-bit register called AL, while the most significant byte of AX can be used as a single 8-bit register called AH. These names refer to the same physical register. When a two-byte quantity is placed into DX, the update affects the value of DH, DL, and EDX. These sub-registers are mainly hold-overs from older, 16-bit versions of the instruction set. However, they are sometimes convenient when dealing with data that are smaller than 32-bits (e.g. 1-byte ASCII characters).

When referring to registers in assembly language, the names are not case-sensitive. For example, the names EAX and eax refer to the same register.

X86 Assembly Manual

Robert G. Plantz

X86 Assembly Manual:

X86 Assembly Language Reference Manual Sun Microsystems, 1995 Modern X86 Assembly Language **Programming** Daniel Kusswurm, 2018-12-06 Gain the fundamentals of x86 64 bit assembly language programming and focus on the updated aspects of the x86 instruction set that are most relevant to application software development This book covers topics including x86 64 bit programming and Advanced Vector Extensions AVX programming The focus in this second edition is exclusively on 64 bit base programming architecture and AVX programming Modern X86 Assembly Language Programming's structure and sample code are designed to help you quickly understand x86 assembly language programming and the computational capabilities of the x86 platform After reading and using this book you ll be able to code performance enhancing functions and algorithms using x86 64 bit assembly language and the AVX AVX2 and AVX 512 instruction set extensions What You Will Learn Discover details of the x86 64 bit platform including its core architecture data types registers memory addressing modes and the basic instruction set Use the x86 64 bit instruction set to create performance enhancing functions that are callable from a high level language C Employ x86 64 bit assembly language to efficiently manipulate common data types and programming constructs including integers text strings arrays and structures Use the AVX instruction set to perform scalar floating point arithmetic Exploit the AVX AVX2 and AVX 512 instruction sets to significantly accelerate the performance of computationally intense algorithms in problem domains such as image processing computer graphics mathematics and statistics Apply various coding strategies and techniques to optimally exploit the x86 64 bit AVX AVX2 and AVX 512 instruction sets for maximum possible performance Who This Book Is For Software developers who want to learn how to write code using x86 64 bit assembly language It s also ideal for software developers who already have a basic understanding of x86 32 bit or 64 bit assembly language programming and are interested in learning how to exploit the *Mastering the Art of x86 Assembly Programming: Unlocking the Secrets of* SIMD capabilities of AVX AVX2 and AVX 512 Expert-Level Skills Steve Jones, 2025-02-25 Unlock the full potential of your programming expertise with Mastering the Art of x86 Assembly Programming Unlocking the Secrets of Expert Level Skills This comprehensive guide is designed for seasoned developers seeking to refine their command of x86 assembly language and leverage its capabilities to the fullest Immerse yourself in in depth discussions on advanced data structures algorithm optimization and intricate memory management techniques that empower you to enhance performance at the lowest level of computing In this meticulously crafted volume explore expert level debugging techniques and systems programming methodologies that reveal the intricacies of hardware interaction and efficient resource utilization Learn to integrate x86 assembly seamlessly with high level languages optimizing the performance and functionality of your applications Delve into secure programming practices that fortify your code against vulnerabilities ensuring robust and resilient solutions in an ever evolving digital landscape Beyond foundational skills this book offers insights into exploiting processor capabilities including SIMD instructions and hardware extensions to

drastically boost computational efficiency With clear explanations real world examples and practical guidance Mastering the Art of x86 Assembly Programming equips you with the tools and knowledge to excel at professional grade software development Whether optimizing existing code or pioneering new applications this indispensable resource will elevate your programming prowess to unprecedented heights The Shellcoder's Handbook Chris Anley, John Heasman, Felix Lindner, Gerardo Richarte, 2011-02-16 This much anticipated revision written by the ultimate group of top security experts in the world features 40 percent new content on how to find security holes in any operating system or application New material addresses the many new exploitation techniques that have been discovered since the first edition including attacking unbreakable software packages such as McAfee s Entercept Mac OS X XP Office 2003 and Vista Also features the first ever published information on exploiting Cisco s IOS with content that has never before been explored The companion Web site features downloadable code files Practical Reverse Engineering Bruce Dang, Alexandre Gazet, Elias Bachaalany, 2014-02-03 Analyzing how hacks are done so as to stop them in the future Reverse engineering is the process of analyzing hardware or software and understanding it without having access to the source code or design documents Hackers are able to reverse engineer systems and exploit what they find with scary results Now the good guys can use the same tools to thwart these threats Practical Reverse Engineering goes under the hood of reverse engineering for security analysts security engineers and system programmers so they can learn how to use these same processes to stop hackers in their tracks The book covers x86 x64 and ARM the first book to cover all three Windows kernel mode code rootkits and drivers virtual machine protection techniques and much more Best of all it offers a systematic approach to the material with plenty of hands on exercises and real world examples Offers a systematic approach to understanding reverse engineering with hands on exercises and real world examples Covers x86 x64 and advanced RISC machine ARM architectures as well as deobfuscation and virtual machine protection techniques Provides special coverage of Windows kernel mode code rootkits drivers a topic not often covered elsewhere and explains how to analyze drivers step by step Demystifies topics that have a steep learning curve Includes a bonus chapter on reverse engineering tools Practical Reverse Engineering Using x86 x64 ARM Windows Kernel and Reversing Tools provides crucial up to date guidance for a broad range of IT professionals

Optimization and Learning Bernabé Dorronsoro, Francisco Chicano, Gregoire Danoy, El-Ghazali Talbi, 2023-05-26 This book constitutes the refereed proceedings of the 6th International Conference on Optimization and Learning OLA 2023 held in Malaga Spain during May 3 5 2023 The 32 full papers included in this book were carefully reviewed and selected from 78 submissions They were organized in topical sections as follows advanced optimization learning methods to enhance optimization tools optimization applied to learning methods and real world applications Practical Malware Analysis Michael Sikorski, Andrew Honig, 2012-02-01 Malware analysis is big business and attacks can cost a company dearly When malware breaches your defenses you need to act guickly to cure current infections and prevent future ones from occurring

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