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Ultra-thin Chip Technology and Applications



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community faces increasingly difficult challenges in the era of silicon nanotechnology and beyond The magnitude of these challenges demands special attention from the metrology and analytical measurements community New paradigms must be found Adequate research and development for new metrology concepts are urgently needed Characterization and metrology are key enablers for developing new semiconductor technology and in improving manufacturing This book summarizes major issues and gives critical reviews of important measurement techniques that are crucial to continuing the advances in semiconductor technology It covers major aspects of process technology and most characterization techniques for silicon research including development manufacturing and diagnostics The book also covers emerging nano devices and the corresponding metrology challenges that arise Micromachining and Microfabrication Process Technology ,2003 5th Electronics Packaging Technology Conference Mahadevan K. Iyer,Kok Chuan Toh,2003 2001 International Symposium on VLSI Technology, Systems, and Applications IEEE, Taipei Section Staff,2001 IEEE/CHMT International Electronic Manufacturing Technology Symposium ,1992

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