

Transistor Level Modeling for Analog/RF IC Design

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 Springer

Transistor Level Modeling For Analogrf Ic Design

Yannis Tsividis, Colin McAndrew



Transistor Level Modeling For Analog IC Design:

Transistor Level Modeling for Analog/RF IC Design Wladyslaw Grabinski, Bart Nauwelaers, Dominique Schreurs, 2006-07-01 Among many great inventions made in the 20th century electronic circuits which later evolved into integrated circuits are probably the biggest when considering their contribution to human society Entering the 21st century the importance of integrated circuits has increased even more In fact without the help of integrated circuits recent high technology society with the internet cellular phone car navigation digital camera and robot would never have been realized Nowadays integrated circuits are indispensable for almost every activity of our society One of the critical issues for the fabrication of integrated circuits has been the precise design of the high speed or high frequency operation of circuits with huge number of components It is quite natural to predict the circuit operation by computer calculation and there have been three waves for this at 15 year intervals The first wave came at the beginning of the 1970s when LSIs Large Scale Integrated circuits with more than 1000 components had just been introduced into the market A mainframe computer was used for the simulation and each semiconductor company used its own proprietary simulators and device models However the capability of the computer and accuracy of the model were far from satisfactory and there are many cases of the necessity of circuit redesign after evaluation of the first chip The second wave hit us in the middle of 1980s when the EWS Engineering Work Station was introduced for use by designers

Operation and Modeling of the MOS Transistor Yannis Tsividis, Colin McAndrew, 2011 The MOS Metal Oxide Semiconductor transistor is the most important building block of modern silicon integrated circuits This book fills an important gap in the literature by presenting a unified treatment of the operation and modeling of the MOS transistor that is complemented with extensive intuitive discussions The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise

Process Variations and Probabilistic Integrated Circuit Design Manfred Dietrich, Joachim Haase, 2011-11-20 Uncertainty in key parameters within a chip and between different chips in the deep sub micron area plays a more and more important role As a result manufacturing process spreads need to be considered during the design process Quantitative methodology is needed to ensure faultless functionality despite existing process variations within given bounds during product development This book presents the technological physical and mathematical fundamentals for a design paradigm shift from a deterministic process to a probability orientated design process for microelectronic circuits Readers will learn to

evaluate the different sources of variations in the design flow in order to establish different design variants while applying appropriate methods and tools to evaluate and optimize their design Compact Modeling Gennady Gildenblat,2010-06-22 Most of the recent texts on compact modeling are limited to a particular class of semiconductor devices and do not provide comprehensive coverage of the field Having a single comprehensive reference for the compact models of most commonly used semiconductor devices both active and passive represents a significant advantage for the reader Indeed several kinds of semiconductor devices are routinely encountered in a single IC design or in a single modeling support group Compact Modeling includes mostly the material that after several years of IC design applications has been found both theoretically sound and practically significant Assigning the individual chapters to the groups responsible for the definitive work on the subject assures the highest possible degree of expertise on each of the covered models **MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch** Viran Jay M. Srivastava,Ghanshyam Singh,2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HfO₂ high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of MOSFETs parameters using image acquisition Organic and Inorganic Light Emitting Diodes T.D. Subash,J. Ajayan,Wladek Grabinski,2023-06-19 This book covers a comprehensive range of topics on the physical mechanisms of LEDs light emitting diodes scattering effects challenges in fabrication and efficient enhancement techniques in organic and inorganic LEDs It deals with various reliability issues in organic inorganic LEDs like trapping and scattering effects packaging failures efficiency droops irradiation effects thermal degradation mechanisms and thermal degradation processes Features Provides insights into the improvement of performance and reliability of LEDs Highlights the optical power improvement mechanisms in LEDs Covers the challenges in fabrication and packaging of LEDs Discusses pertinent failures and degradation mechanisms Includes droop minimization techniques This book is aimed at researchers and graduate students in LEDs illumination engineering optoelectronics and polymer organic materials Circuits and Systems Advances in Near Threshold Computing Sanghamitra Roy,2021-05-11 Modern society is witnessing a sea change in ubiquitous computing in which people have embraced computing systems as an indispensable part of day to day existence Computation storage and communication abilities of smartphones for example have undergone monumental changes over the past decade However global emphasis on creating and sustaining green environments is leading to a rapid and ongoing proliferation of edge computing systems and applications As a broad spectrum of healthcare home and transport applications shift to the edge of the network near threshold computing NTC is emerging as one of the promising low power computing platforms An NTC

device sets its supply voltage close to its threshold voltage dramatically reducing the energy consumption Despite showing substantial promise in terms of energy efficiency NTC is yet to see widescale commercial adoption This is because circuits and systems operating with NTC suffer from several problems including increased sensitivity to process variation reliability problems performance degradation and security vulnerabilities to name a few To realize its potential we need designs techniques and solutions to overcome these challenges associated with NTC circuits and systems The readers of this book will be able to familiarize themselves with recent advances in electronics systems focusing on near threshold computing

Springer Handbook of Semiconductor Devices Massimo Rudan, Rossella Brunetti, Susanna Reggiani, 2022-11-10 This Springer Handbook comprehensively covers the topic of semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly 100 leading scientists from industry and academia were selected to write the handbook's chapters which were conceived for professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook proceeds with a section fully devoted to the main conventional semiconductor devices like e g bipolar transistors and MOS capacitors and transistors used in the production of the standard integrated circuits and the corresponding physical models In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook

CMOS Analog Design Using All-Region MOSFET Modeling Márcio Cherem Schneider, Carlos Galup-Montoro, 2010-01-28 Covering the essentials of analog circuit design this book takes a unique design approach based on a MOSFET model valid for all operating regions rather than the standard square law model Opening chapters focus on device modeling integrated circuit technology and layout whilst later chapters go on to cover noise and mismatch and analysis and design of the basic building blocks of analog circuits such as current mirrors voltage references voltage amplifiers and operational amplifiers An introduction to continuous time filters is also provided as are the basic principles of sampled data circuits especially switched capacitor circuits The final chapter then reviews MOSFET models and describes techniques to extract design parameters With numerous design examples and exercises also included this is ideal for students taking analog CMOS design courses and also for circuit designers who need to shorten the design cycle

Nanoelectronic Mixed-Signal System Design Saraju Mohanty, 2015-02-20 Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics

category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability *RF Circuit Design* Christopher Bowick, 2011-04-08 It s Back New chapters examples and insights all infused with the timeless concepts and theories that have helped RF engineers for the past 25 years RF circuit design is now more important than ever as we find ourselves in an increasingly wireless world Radio is the backbone of today s wireless industry with protocols such as Bluetooth Wi Fi WiMax and ZigBee Most if not all mobile devices have an RF component and this book tells the reader how to design and integrate that component in a very practical fashion This book has been updated to include today s integrated circuit IC and system level design issues as well as keeping its classic wire lead material Design Concepts and Tools Include The Basics Wires Resistors Capacitors Inductors Resonant Circuits Resonance Insertion Loss Filter Design High pass Bandpass Band rejection Impedance Matching The L Network Smith Charts Software Design Tools Transistors Materials Y Parameters S Parameters Small Signal RF Amplifier Transistor Biasing Y Parameters S Parameters RF Power Amplifiers Automatic Shutdown Circuitry Broadband Transformers Practical Winding Hints RF Front End Architectures Software Defined Radios ADC s Effects RF Design Tools Languages Flow Modeling Check out this book s companion Web site at <http://www.elsevierdirect.com/companion.jsp> ISBN 9780750685184 for full color Smith Charts and extra content Completely updated but still contains its classic timeless information Two NEW chapters on RF Front End Design and RF Design Tools Not overly math intensive perfect for the working RF and digital professional that need to build analog RF Wireless circuits

Charge-Based MOS Transistor Modeling Christian C. Enz, Eric A. Vittoz, 2006-08-21 Modern large scale analog integrated circuits ICs are essentially composed of metal oxide semiconductor MOS transistors and their interconnections As technology scales down to deep sub micron dimensions and supply voltage decreases to reduce power consumption these complex analog circuits are even more dependent on the exact behavior of each transistor High performance analog circuit design requires a very detailed model of the transistor describing accurately its static and dynamic behaviors its noise and matching limitations and its temperature variations The charge based EKV Enz Krummenacher Vittoz MOS transistor model for IC design has been developed to provide a clear understanding of the device properties without the use of complicated equations All the static dynamic noise non quasi static models are completely

described in terms of the inversion charge at the source and at the drain taking advantage of the symmetry of the device. Thanks to its hierarchical structure the model offers several coherent description levels from basic hand calculation equations to complete computer simulation model. It is also compact with a minimum number of process dependant device parameters. Written by its developers this book provides a comprehensive treatment of the EKV charge based model of the MOS transistor for the design and simulation of low power analog and RF ICs. Clearly split into three parts the authors systematically examine the basic long channel intrinsic charge based model including all the fundamental aspects of the EKV MOST model such as the basic large signal static model, the noise model and a discussion of temperature effects and matching properties, the extended charge based model presenting important information for understanding the operation of deep submicron devices, the high frequency model setting out a complete MOS transistor model required for designing RF CMOS integrated circuits. Practising engineers and circuit designers in the semiconductor device and electronics systems industry will find this book a valuable guide to the modelling of MOS transistors for integrated circuits. It is also a useful reference for advanced students in electrical and computer engineering.

Integrated Circuit and System Design, 2003
Digest of Technical Papers, 2001 **XI Brazilian Symposium on Integrated Circuit Design** Marcelo Lubaszewski, Vladimir Castro Alves, 1998. Topics in this book on integrated circuit design include hardware software codesign of embedded systems, the ALFA HUERTA project, rapid prototyping, digital testing and digital design. **Proceedings of the IEEE 1999 Custom Integrated Circuits Conference**, 1999 *1996 IEEE-CAS Region 8 Workshop on Analog and Mixed IC Design* Franco Maloberti, Andrea Baschiroto, Valentino Liberali, 1996 Portable Design, 2005 **EDN, Electrical Design News**, 2003 **Semiconductor International**, 2007

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