# VLSI DESIGN LABORATORY MANUAL



## Course Coordinator

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VASIREDDY VENKATADRI INSTITUTE OF TECHNOLOGY::NAMBUR (Autonomous)
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# **Vlsi Design Lab Manual**

Mr. Rohit Manglik

#### Vlsi Design Lab Manual:

AN INTRODUCTION TO VLSI DESIGN AND TESTING P. Brundavani ,B. Abdul Raheem ,Rachapudi Prabahakar ,2025-05-20 VLSI Design and Testing provides a concise yet comprehensive guide to the design analysis and testing of integrated circuits Covering key topics such as IC types Moore's Law MOSFET and CMOS fabrication and SOI technology the book builds a strong foundation in VLSI principles It explores the design flow CMOS logic gates layout techniques and both static and dynamic logic circuits Readers will also learn about circuit performance parameters scaling theory and subsystem design including adders shifters and comparators The book concludes with essential concepts in VLSI design styles FPGA gate array full custom and CMOS testing including fault models ATPG and BIST Ideal for students and professionals it blends theory with practical design strategies in modern VLSI systems Visit garuda publishers com **Microwave and Optical Communications Lab Manual** Mr. Rohit Manglik, 2024-04-06 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels Lab Manual Latest Edition Dr. J. P. Goel, 2016-12-17 Lab E Manual Physics For XIIth Practicals A Every student will perform 10 experiments 5 from each section 8 activities 4 from each section during the academic year Two demonstration experiments must be performed by the teacher with participation of students The students will maintain a record of these demonstration experiments B Evaluation Scheme for Practical Examination One experiment from any one section 8 Marks Two activities one from each section 4 4 8 Marks Practical record experiments activities 6 Marks Record of demonstration experiments Viva based on these experiments 3 Marks Viva on experiments activities 5 Marks Total 30 Marks Section A Experiments 1 To determine resistance per cm of a given wire by plotting a graph of potential difference versus current 2 To find resistance of a given wire using metre bridge and hence determine the specific resistance of its material 3 To verify the laws of combination series parallel of resistances using a metre bridge 4 To compare the emf of two given primary cells using potentiometer 5 To determine the internal resistance of given primary cells using potentiometer 6 To determine resistance of a galvanometer by half deflection method and to find its figure of merit 7 To convert the given galvanometer of known resistance and figure of merit into an ammeter and voltmeter of desired range and to verify the same 8 To find the frequency of the a c mains with a sonometer Activities 1 To measure the resistance and impedance of an inductor with or without iron core 2 To measure resistance voltage AC DC current AC and check continuity of a given circuit using multimeter 3 To assemble a household circuit comprising three bulbs three on off switches a fuse and a power source 4 To assemble the components of a given electrical circuit 5 To study the variation in potential drop with length of a wire for a steady current 6 To draw the diagram of a given open circuit comprising at least a battery resistor rheostat key ammeter and voltmeter Mark the components that are not connected in proper order and correct the circuit and

also the circuit diagram Section B Experiments 1 To find the value of v for different values of u in case of a concave mirror and to find the focal length 2 To find the focal length of a convex lens by plotting graphs between u and v or between 1 u and 1 u 3 To find the focal length of a convex mirror using a convex lens 4 To find the focal length of a concave lens using a convex lens 5 To determine angle of minimum deviation for a given prism by plotting a graph between angle of incidence and angle of deviation 6 To determine refractive index of a glass slab using a travelling microscope 7 To find refractive index of a liquid by using i concave mirror ii convex lens and plane mirror 8 To draw the I V characteristic curve of a p n junction in forward bias and reverse bias 9 To draw the characteristic curve of a zener diode and to determine its reverse break down voltage 10 To study the characteristics of a common emitter npn or pnp transistor and to find out the values of current and voltage gains Activitie 1 To study effect of intensity of light by varying distance of the source on a L D R 2 To identify a diode a LED a transistor and IC a resistor and a capacitor from mixed collection of such items 3 Use of multimeter to i identify base of transistor ii distinguish between npn and pnp type transistors iii see the unidirectional flow of current in case of a diode and a LED iv check whether a given electronic component e g diode transistor or I C is in working order 4 To observe refraction and lateral deviation of a beam of light incident obliquely on a glass slab 5 To observe polarization of liquid using two Polaroids 6 To observe diffraction of light due to a thin slit 7 To study the nature and size of the image formed by i convex lens ii concave mirror on a screen by using a candle and a screen for different distances of the candle from the lens mirror 8 To obtain a lens combination with the specified focal length by using two lenses from the given set of lenses Suggested Investigatory Projects 1 To investigate whether the energy of a simple pendulum is conserved 2 To determine the radius of gyration about the centre of mass of a metre scale as a bar pendulum 3 To investigate changes in the velocity of a body under the action of a constant force and determine its acceleration 4 To compare effectiveness of different materials as insulators of heat 5 To determine the wavelengths of laser beam by diffraction 6 To study various factors on which the internal resistance emf of a cell depends 7 To construct a time switch and study dependence of its time constant on various factors 8 To study infrared radiations emitted by different sources using photo transistor 9 To compare effectiveness of different materials as absorbers of sound 10 To design an automatic traffic signal system using suitable combination of logic gates 11 To study luminosity of various electric lamps of different powers and make 12 To compare the Young s modulus of elasticity of different specimens of rubber and also draw their elastic hysteresis curve 13 To study collision of two balls in two dimensions 14 To study frequency response of i a resistor an inductor and a capacitor ii RL circuit iii RC circuit iv LCR series circuit VLSI Design ,1984 Microelectronics Education B. Courtois, N. Guillemot, G. Kamarinos, G. Stéhelin, 2013-03-09 This is the third edition of the European Workshop on Microelectronics Education EWME A steady state regime has now been reached An international community of university teachers is constituted they exchange their experience and their pedagogical tools They discuss the best ways to transfer the rapidly changing techniques to their

students and to introduce them to the new physical and mathematical concepts and models for the innovative techniques devices circuits and design methods The number of abstracts submitted to EWME 2000 about one hundred enabled the scientific committee to proceed to a clear selection EWME is a European meeting Indeed authors from 20 different European countries contribute to this volume Nevertheless the participation of authors from Brazil Canada China New Zealand and USA shows that the workshop gradually attains an international dimension th The 20 century can be characterized as the century of electron The electron as an elementary particle was discovered by J.J. Thomson in 1897 and was rapidly used to transfer energy and information Thanks to electron universe and micro cosmos could be explored Electron became the omnipotent and omnipresent almost immaterial angel of our W orld This was made possible thanks to electronics and for the last 30 years to microelectronics Microelectronics not only modified and even radically transformed the industrial and the every day landscapes but it also led to the so called information revolution with which begins the 21 st century Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike Scientific and Technical Aerospace Reports .1995 Computer Algebra Handbook Johannes Grabmeier, Erich Kaltofen, Volker Weispfenning, 2012-12-06 Two ideas lie gleaming on the jeweler's velvet The first is the calculus the sec ond the algorithm The calculus and the rich body of mathematical analysis to which it gave rise made modern science possible but it has been the algorithm that has made

possible the modern world David Berlinski The Advent of the Algorithm First there was the concept of integers then there were symbols for integers I II III 1111 fttt what might be called a sticks and stones representation I II III IV V Roman numerals 1 2 3 4 5 Arabic numerals etc Then there were other concepts with symbols for them and algorithms sometimes for ma nipulating the new symbols Then came collections of mathematical knowledge tables of mathematical computations theorems of general results Soon after algorithms came devices that provided assistancefor carryingout computations Then mathematical knowledge was organized and structured into several related concepts and symbols logic algebra analysis topology algebraic geometry number theory combinatorics etc This organization and abstraction lead to new algorithms and new fields like universal algebra But always our symbol systems reflected and influenced our thinking our concepts and our algorithms The Cumulative Book Index ,1990 A world list of books in the English language Readings in Hardware/Software Co-Design Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction ans reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

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**Subject Guide to Books in Print**, 2001 Advances in Information and Communication Networks Kohei Arai, Supriva Kapoor, Rahul Bhatia, 2018-12-05 The book gathering the proceedings of the Future of Information and Communication Conference FICC 2018 is a remarkable collection of chapters covering a wide range of topics in areas of information and communication technologies and their applications to the real world It includes 104 papers and posters by pioneering academic researchers scientists industrial engineers and students from all around the world which contribute to our understanding of relevant trends of current research on communication data science ambient intelligence networking computing security and Internet of Things This book collects state of the art chapters on all aspects of information science and communication technologies from classical to intelligent and covers both theory and applications of the latest technologies and methodologies Presenting state of the art intelligent methods and techniques for solving real world problems along with a vision of the future research this book is an interesting and useful resource **Japanese Technical** Abstracts, 1987 Publisher's Monthly ,1999 **Current Trends in Hardware Verification and Automated Theorem Proving** Graham Birtwistle, P.A. Subrahmanyam, 2012-12-06 This report describes the partially completed correctness proof of the Viper block model Viper 7 8 9 11 23 is a microprocessor designed by W J Cullyer C Pygott and J Kershaw at the Royal Signals and Radar Establishment in Malvern England henceforth RSRE for use in safety critical applications such as civil aviation and nuclear power plant control It is currently finding uses in areas such as the de ployment of weapons from tactical aircraft To support safety critical applications Viper has a particulary simple design about which it is relatively easy to reason using current techniques and models The designers who deserve much credit for the promotion of formal methods intended from the start that Viper be formally verified Their idea was to model Viper in a sequence of decreasingly abstract levels each of which concentrated on some aspect of the design such as the flow of control the processing of instructions and so on That is each model would be a specification of the next less abstract model and an implementation of the previous model if any The verification effort would then be simplified by being structured according to the sequence of abstraction levels These models or levels of description were characterized by the design team The first two levels and part of the third were written by them in a logical language amenable to reasoning and proof ASIC & EDA ,1994

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